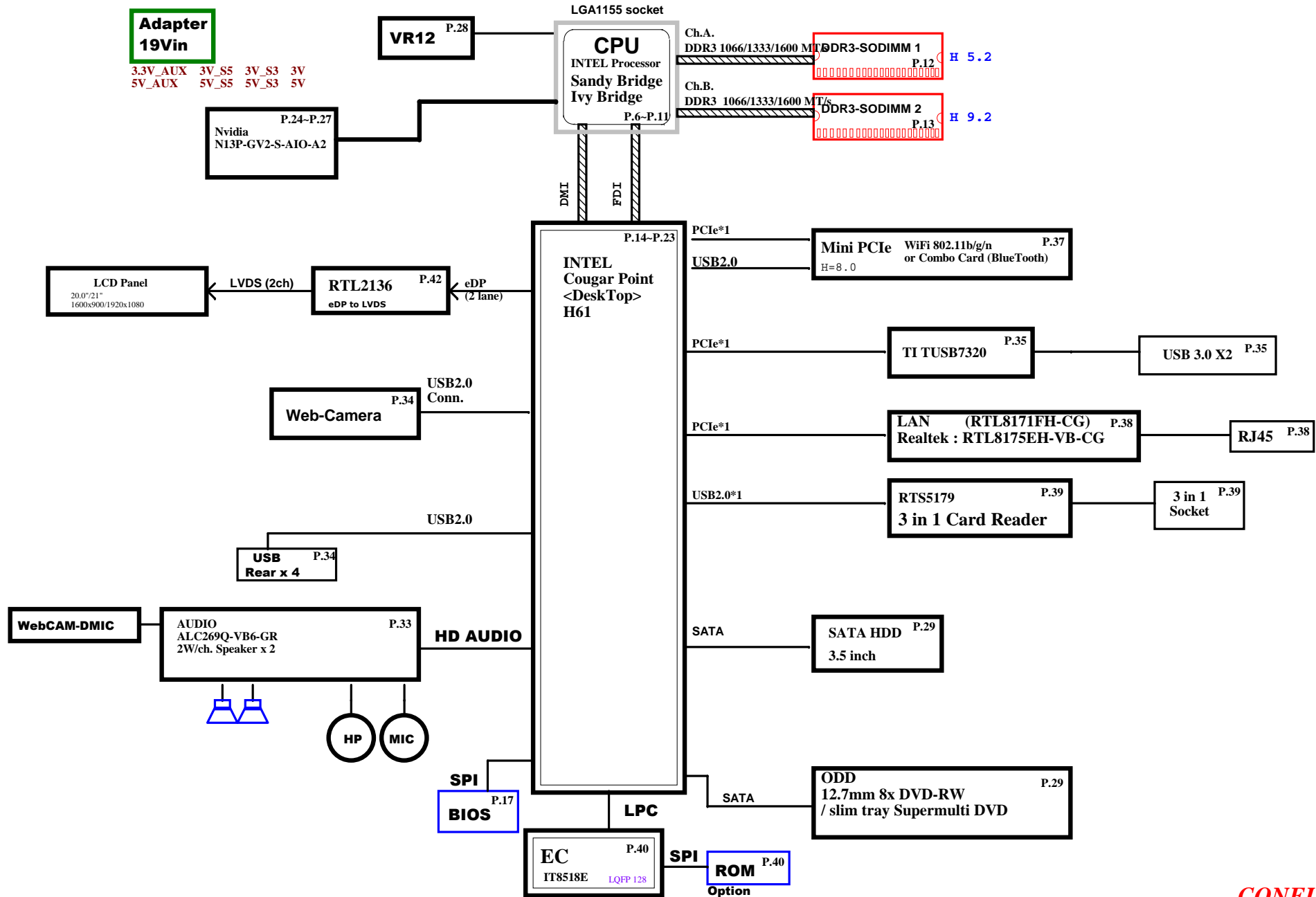


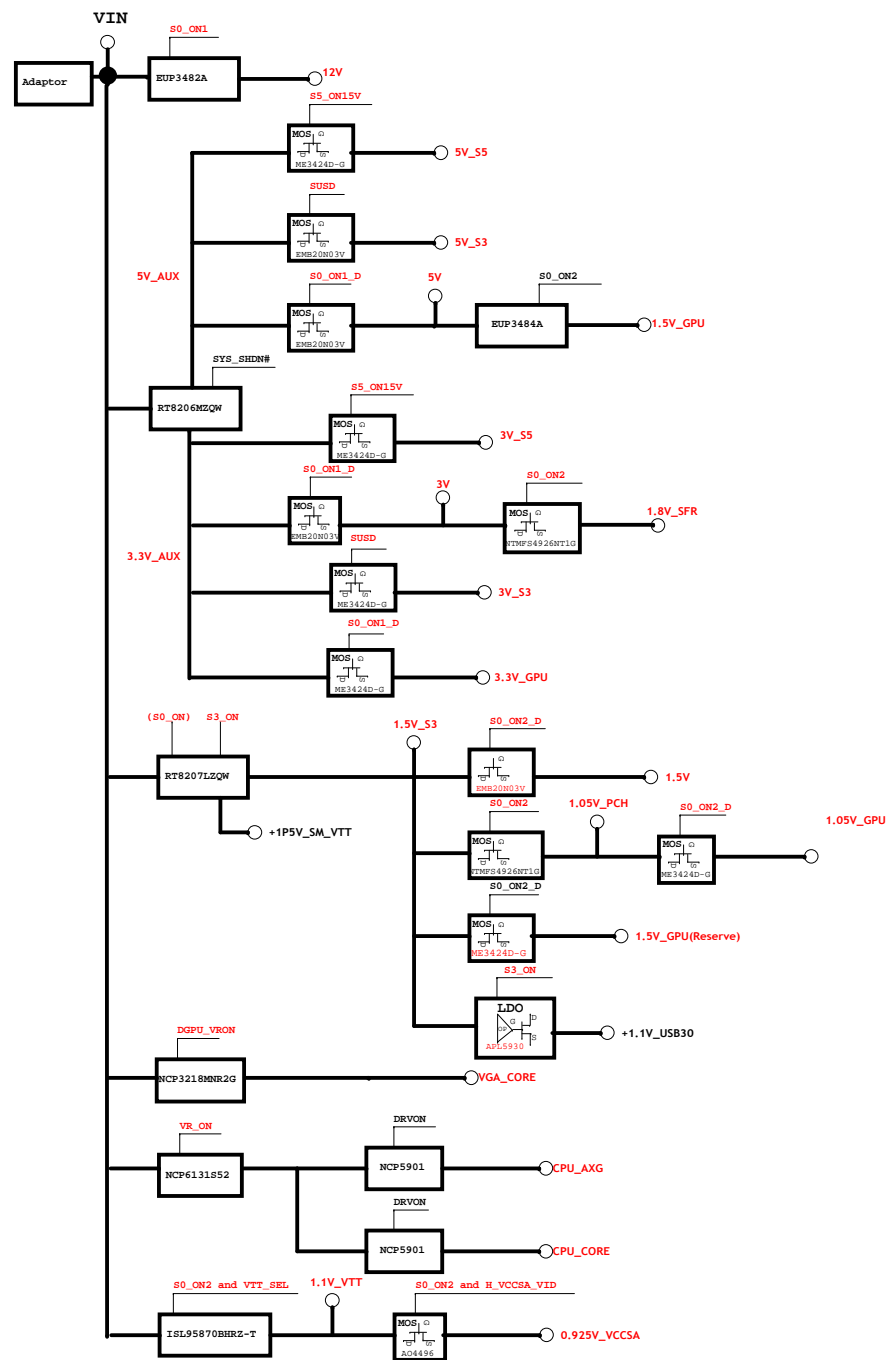
Xiamen System Block Diagram



www.vinafix.com

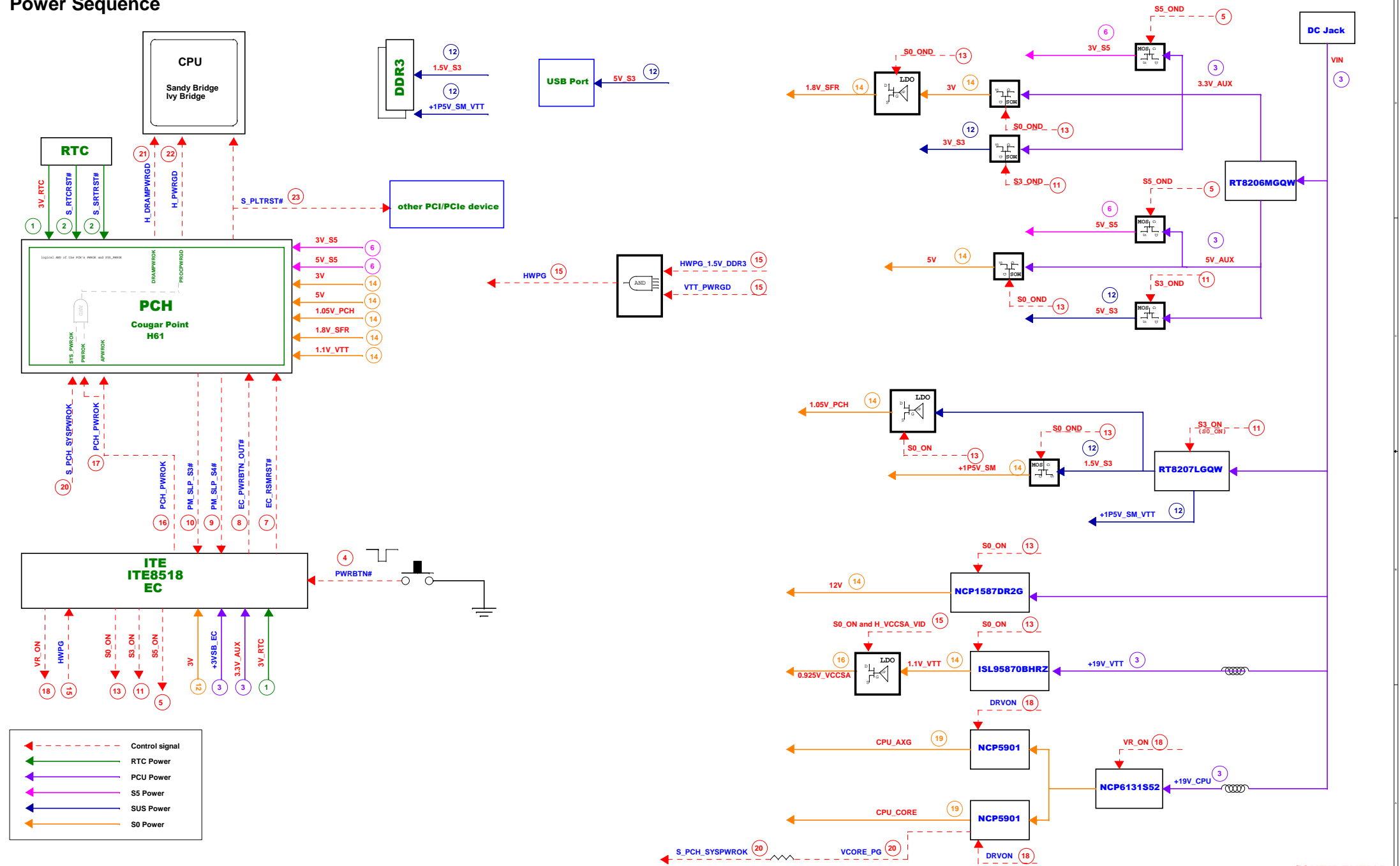
CONFIDENTIAL

Power Rail	Destination	Voltage	S0 Current
CPU_CORE	Sandy Bridge: CPU core 65W	0.65V-1.3V	75A
CPU_AXG	Sandy Bridge: CPU AXG	0.5 V-1.3V	35A
0.925V_VCCSA	System Agent	0.925 V/0.85 V	8.8A
1.1V_VTT	Sandy Bridge : Memory controller PCH : DMI PCH : CPU_IO	Hi-->1.05V 1.1V Low-->1.1V 1.05V-1.1V-1.16V	8.5A (TDC) 0.065A 0.001A
1.8V_SFR	Sandy Bridge: Internal processor PLL	1.71V-1.8V-1.89V	1.6A
1.5V_S3	Sandy Bridge: CPU I/O Voltage for DDRIII DIMM :	1.425V-1.5V-1.575V	4.75A
+IP5V_SM_VTT	DDRIII Terminator:	0.75V	2A
1.05V_PCH	PCH :PCH_1.05V PCH : Vcc core I/O buffer PCH : DMI buffer voltage PCH : Display PLL A power PCH : Display PLL B power	0.998V-1.05V-1.1V 0.998V-1.05V-1.1V 0.998V-1.05V-1.1V 0.998V-1.05V-1.1V 0.998V-1.05V-1.1V	1.31A 2.1+4.35A 0.057A 0.08A 0.08A
+1P5V_SM	Mini PCIE : +1.5V(WLAN)	1.425V-1.5V-1.575V	0.5A
VIN	CONVERTER	17V-19V-21V	1.6A
3V_S3	WebCam	3.0V-3.3V-3.6V	0.12A
3V	PCH: I/O buffer voltage PCH: Display DAC Analog power ALC269 : DVD Mini PCIE : +3.3V(WLAN) FAN_RAM RTS21365 RTL8175EH RTS5179	3.14V-3.3V-3.47V 3.135V-3.3V-3.465V 3.135V-3.3V-3.465V 3.0V-3.3V-3.6V 3.0V-3.3V-3.6V 3.0V-3.3V-3.6V	0.267A 0.068A 0.0228A 2.75A 0.23A 0.11A 0.070A 0.045A
5V	PCH: Core well Ref. voltage SATA ODD SATA HDD(3.5" x HDD) LCD Panel (SAMSUNG) 5V Audio	4.75V-5V-5.25V 4.75V-5V-5.25V 4.75V-5V-5.25V 4.5V-5V-5.5V 4.75V-5V-5.25V	0.001A 1.5A 0.4A 1.08A 0.040A
5V_S3	USB: *4 ports	5V	2A
12V	HDD FAN_CPU	11.4V-12V-12.6V 12V	0.85A 0.44A
3V_S5	PCH : Intel Management Engine PCH : Suspend well I/O Buffer PCH : HD Audio controller EC(IT8518) : VSTBY SPI FLASH ROM	3.14V-3.3V-3.47V 3.14V-3.3V-3.47V 3.14V-3.3V-3.47V 3.0V-3.3V-3.6V	0.097A 0.168A 0.010A 0.020A
5V_S5 (*REF5V_010)	PCH : Suspend well Ref. Voltage	4.75V-5V-5.25V	0.001A
3.3V_AUX (*3VSB_BC)	EC(IT8518) : VPCU	3.0V-3.3V-3.6V	



CONFIDENTIAL

Power Sequence

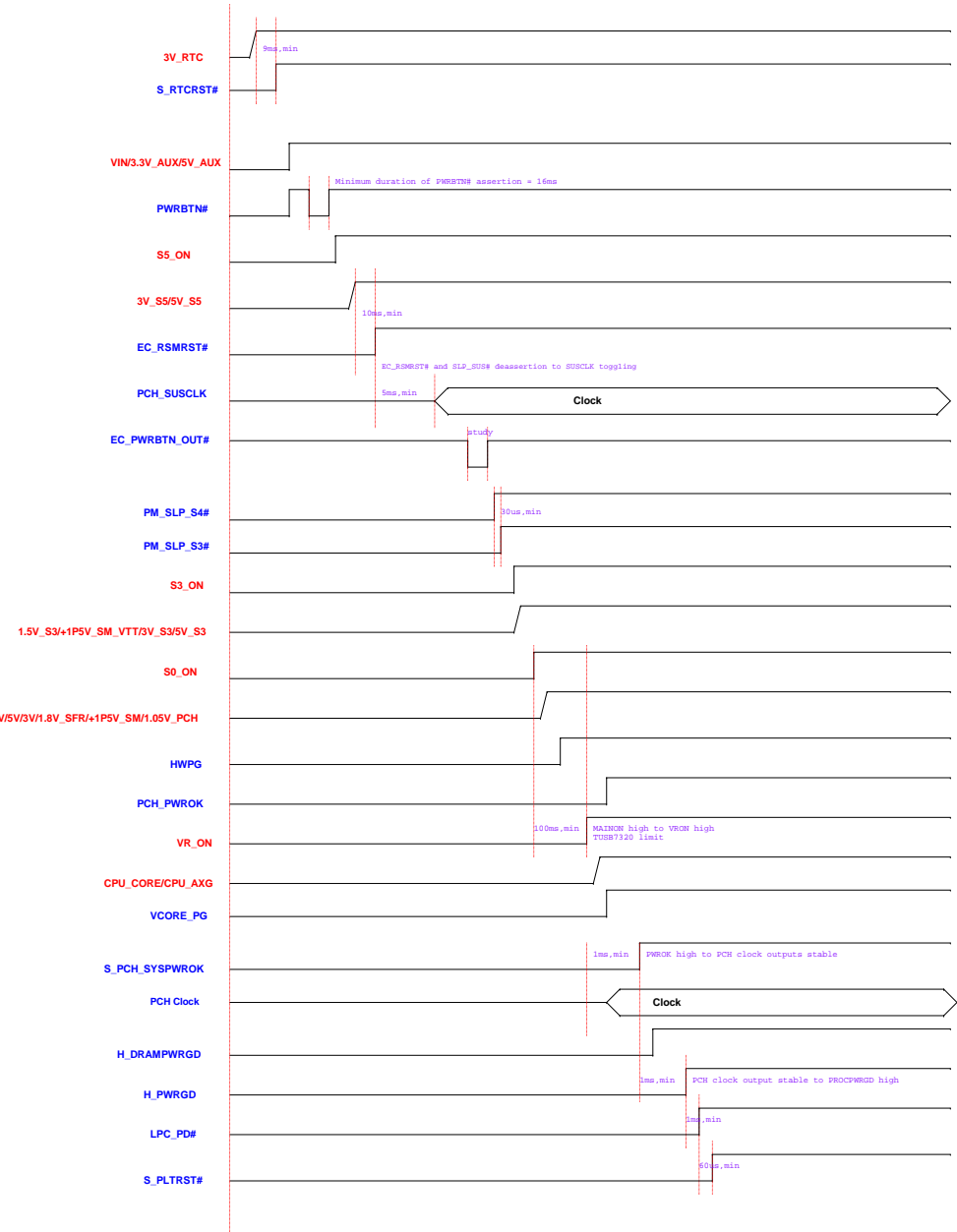


CONFIDENTIAL

POWER SEQUENCE

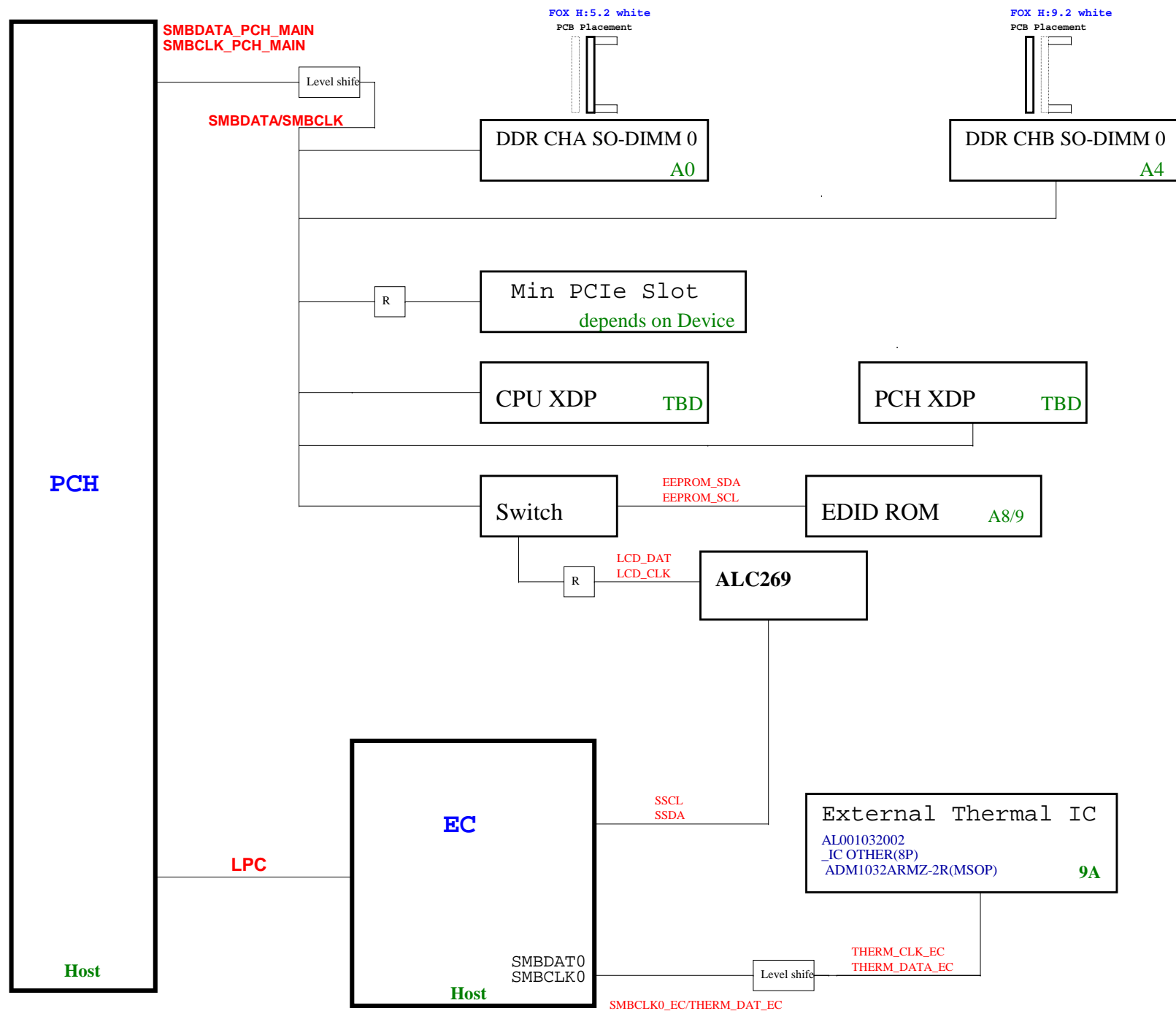
Voltage Rails

Power	Voltage	S0	S3	S4	S5	PCU	G3	Ctrl Signal	
3V_RTC	3V	ON	ON	ON	ON	ON	ON		RTC, PCH
1W	19V	ON	ON	ON	ON	ON	OFF	Adaptor in	
5V_AUX	5V	ON	ON	ON	ON	ON	OFF	Adaptor in	
3.3V_AUX	3.3V	ON	ON	ON	ON	ON	OFF	Adaptor in	EC, Flash
5V_S5	5V	ON	ON	ON	ON	OFF	OFF	S5_ON15V	PCH, AMP
3V_S5	3.3V	ON	ON	ON	ON	OFF	OFF	S5_ON15V	PCH, XDP, SPI flash ROM
5V_S3	5V	ON	ON	OFF	OFF	OFF	OFF	SUS0	USB2.0
3V_S3	3.3V	ON	ON	OFF	OFF	OFF	OFF	SUS0	WEBCAM
1.5V_S3	1.5V	ON	ON	OFF	OFF	OFF	OFF	S3_ON	DDR3, CPU DDR3 I/O
+1PSV_SM_VTT	0.75V	ON	ON	OFF	OFF	OFF	OFF	S3_ON	DDR3
+1.1V_USB30	1.1V	ON	ON	OFF	OFF	OFF	OFF	S3_ON	USB3.0
12V	12V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON1	FAN, CPU, Panel, HDD,
5V	5V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON1_D	PCH, CRT, ODD, HDD, Panel
3V	3.3V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON1_D	WL, RTS21365, RTL8175EH, RTS5179 , Codec, PCH, DDR3, Flash, EEPROM, FAN, RAM
3.3V_GPU	3.3V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON1_D	TUSB3230
1.05V_PCH	1.05V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON2	PCH_I/O, PCH_CLK, PCH_PLL, PCH_CORE
1.8V_SFR	1.8V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON2	PCH, CPU_PLL
1.5V_GPU	1.5V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON2	GPU
1.5V	1.5V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON2_D	WL
1.5V_GPU	1.5V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON2_D	GPU
1.05V_GPU	1.05V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON2_D	GPU
1.1V_VTT	1.05V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON2	XDP, PCH_DM, PCH_PROIO, CPU_I/O
0.925V_VCCSA	0.925 /0.85 V	ON	OFF	OFF	OFF	OFF	OFF	S0_ON2	CPU_SA
CPU_AXG	0.5-1.3V	ON	OFF	OFF	OFF	OFF	OFF	DRVON	CPU_AXG
CPU_CORE	0.65-1.3V	ON	OFF	OFF	OFF	OFF	OFF	DRVON	CPU_Core

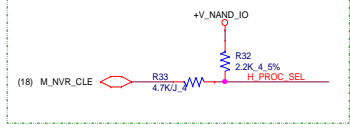
[illegible]

CONFIDENTIAL

SMBus Block Diagram

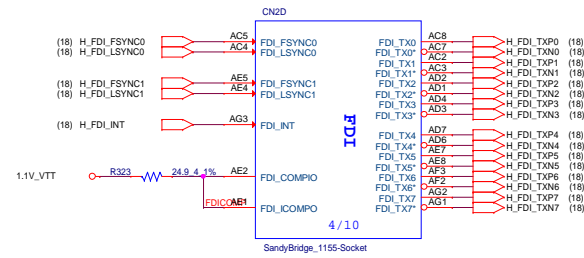


CONFIDENTIAL



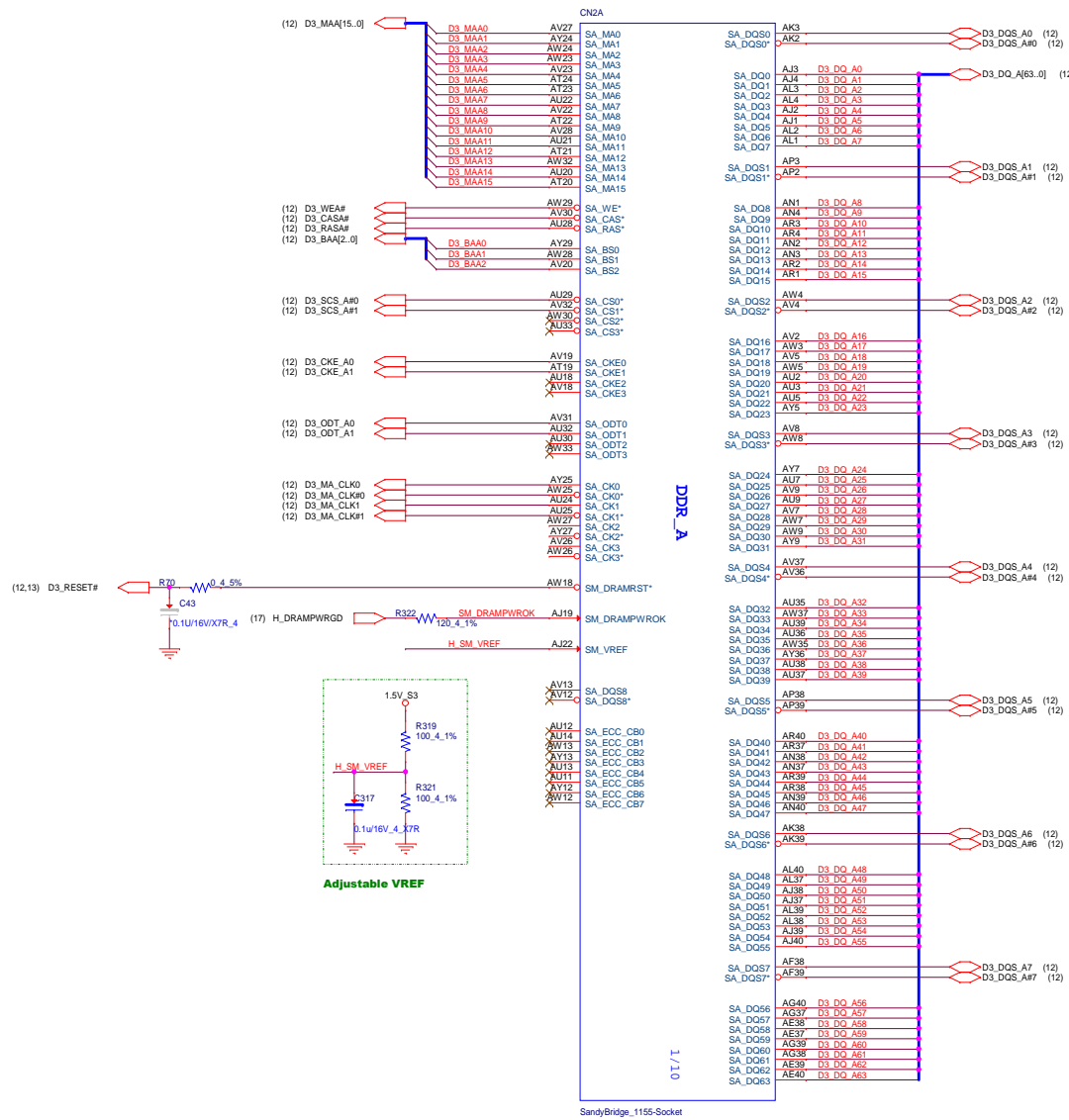
CPU_VTT	
	VTT_SEL
HI	1.05V
LO	1.0V

 **Quanta Computer Inc.**
PROJECT : Xiamen/WJB

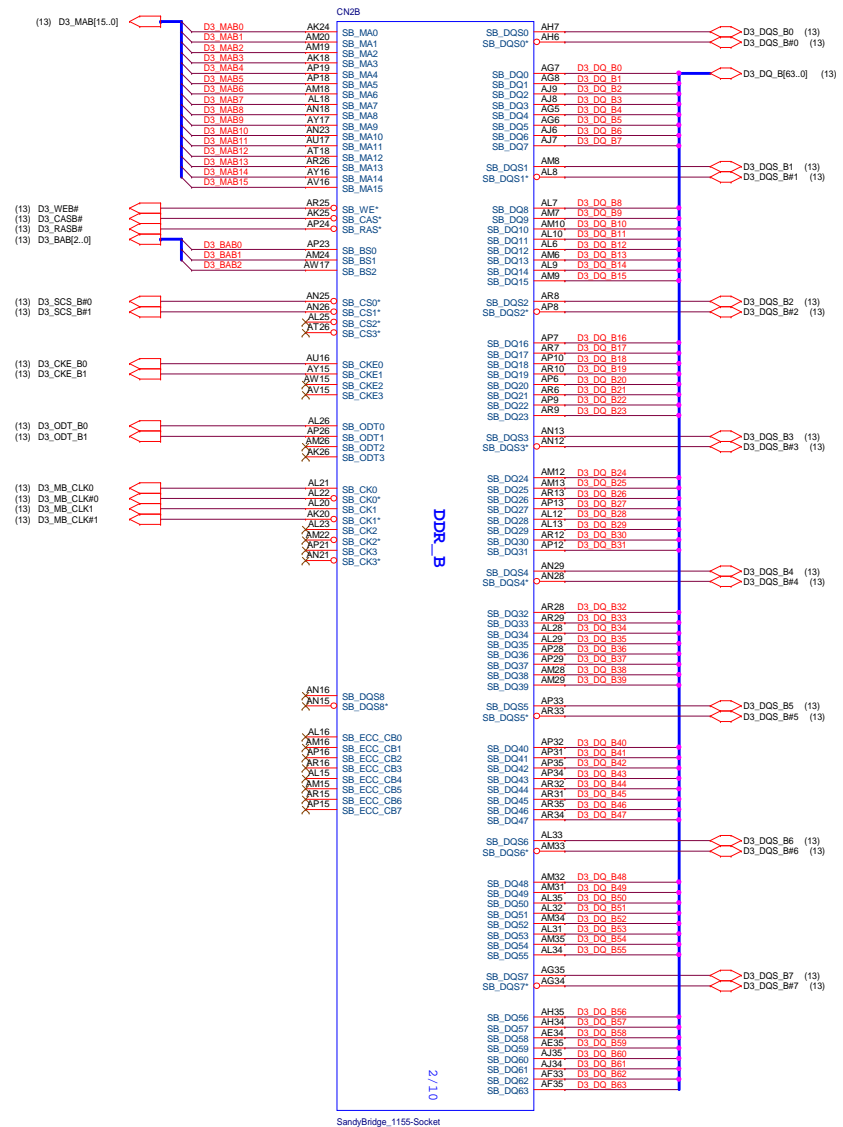


Quanta Computer Inc.
PROJECT : Xiamen/WJB

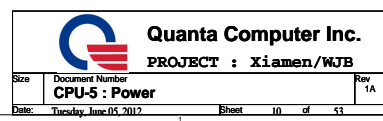
Size	Document Number CPU-2 : FDI/PCIe/DMI	Rev 1A
Date: Tuesday, June 05, 2012	Sheet 7 of 53	

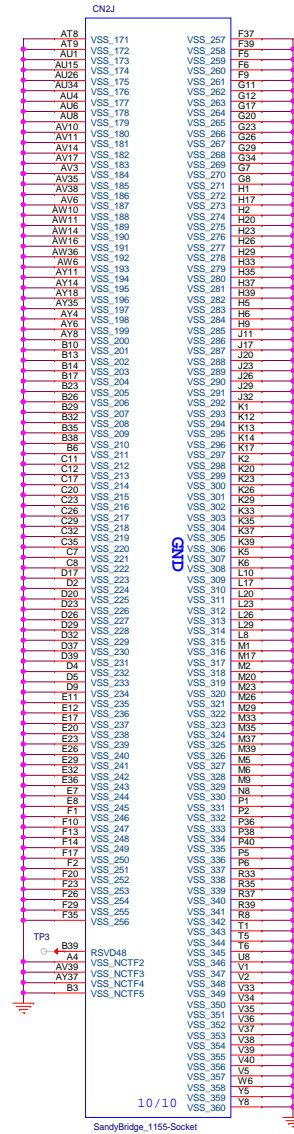
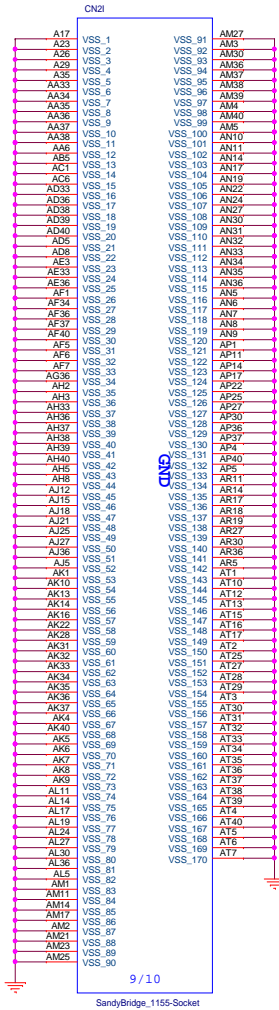


CONFIDENTIAL



CONFIDENTIAL



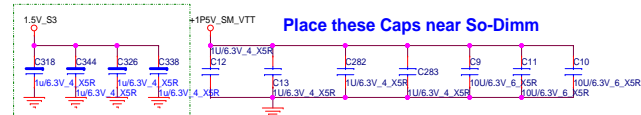
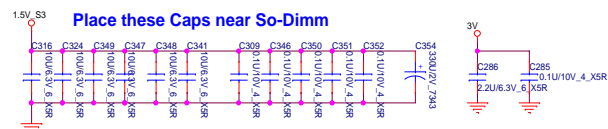
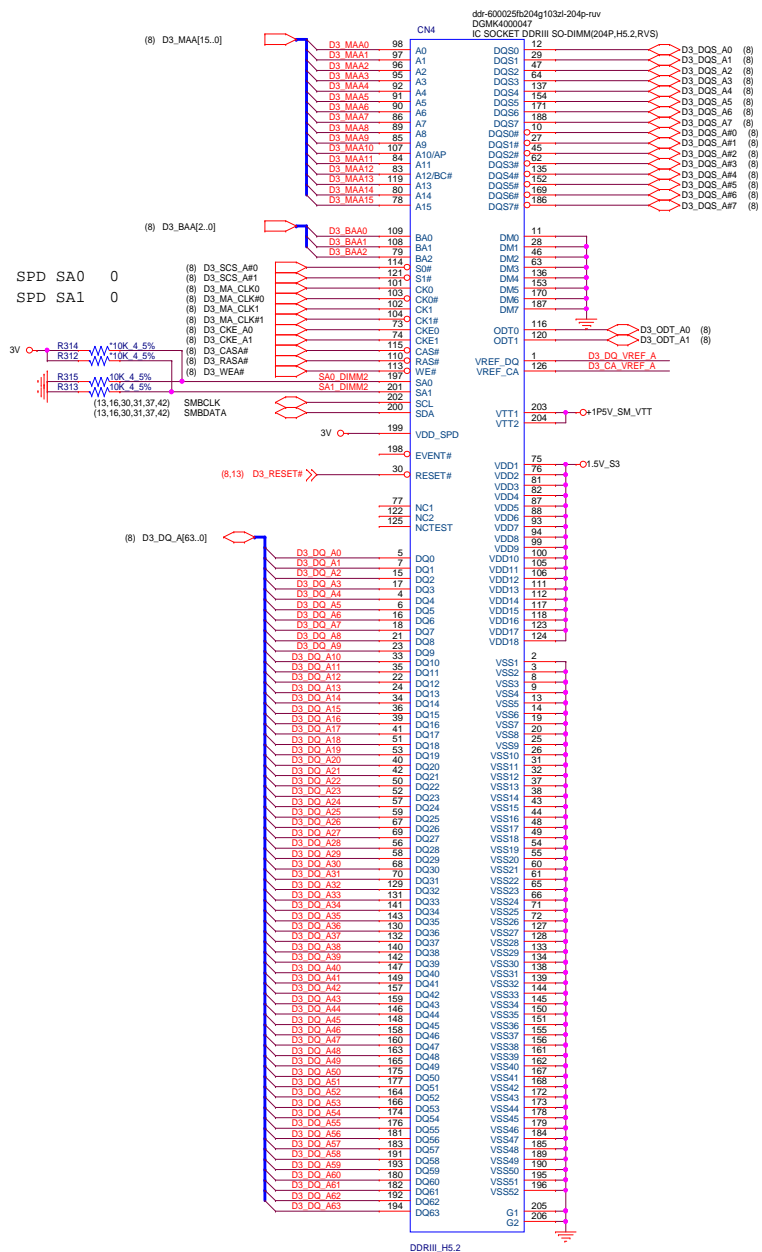
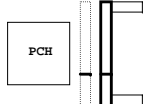
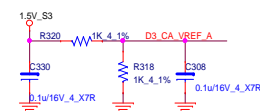
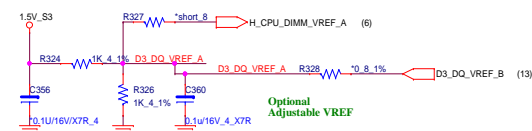


CONFIDENTIAL

CHANNEL A
SMB ADDRESS:000

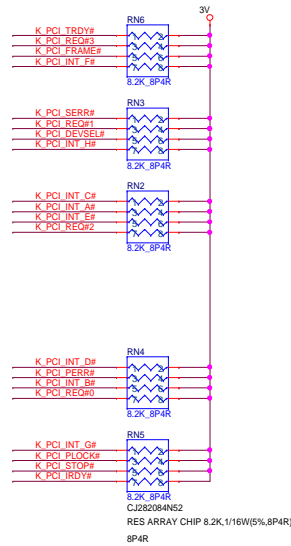
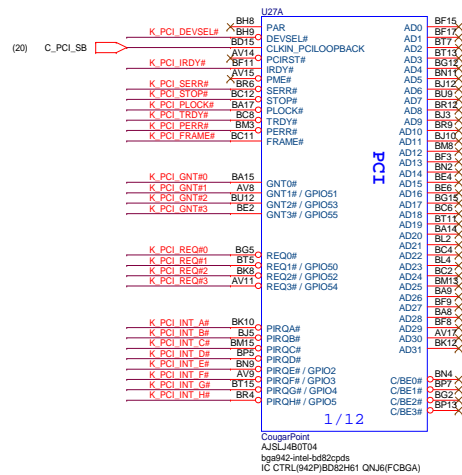
H:5.2 RVS Black

PCB Placement

**PLAECCE CLOSE TO CH-A DIMM**

CONFIDENTIAL

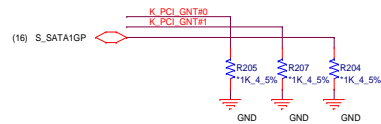
No PCI Device



Boot BIOS Select

Boot Device	K_PCI_GNT#1	S_SATA1GP
LPC	0	0
PCI	1	0
MBAND	0	1
SPI	1	1

Chipset internal pull high

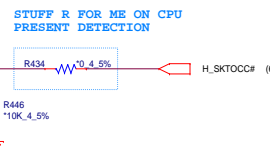


GNT3# Internal pull-up.



GNT2#/GPIO53:ESI strap for server platform
ONLY,Do not pull low.

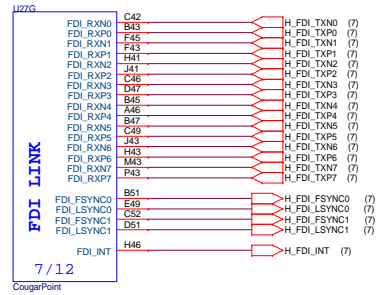
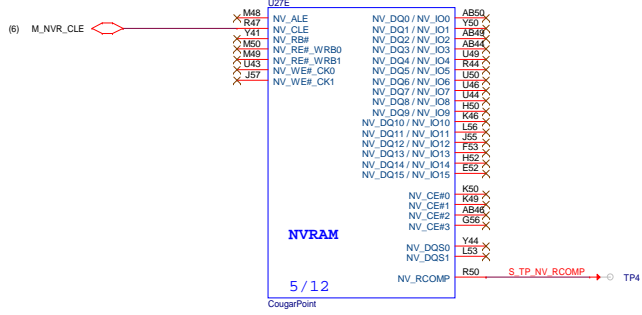
CONFIDENTIAL

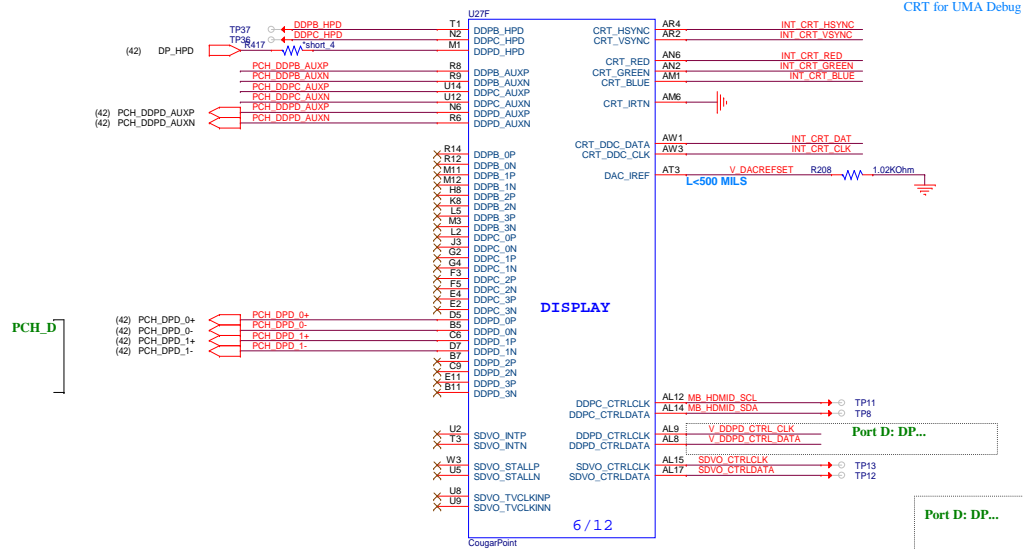


Panel ID	High	LOW
	20"	21.5"

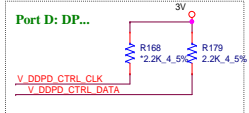
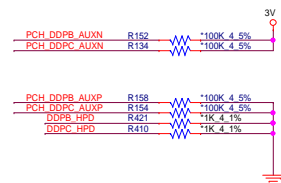
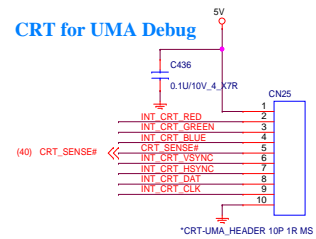


S_NVR_CLE internal pull-down.

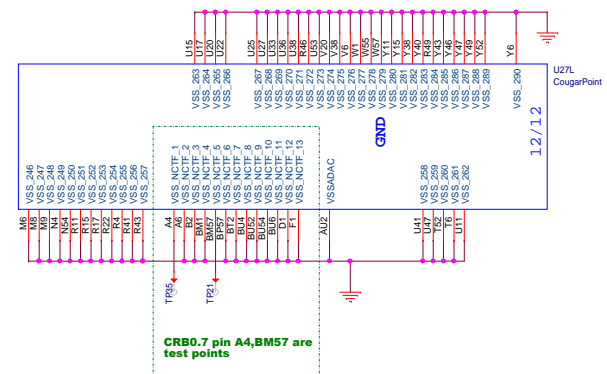
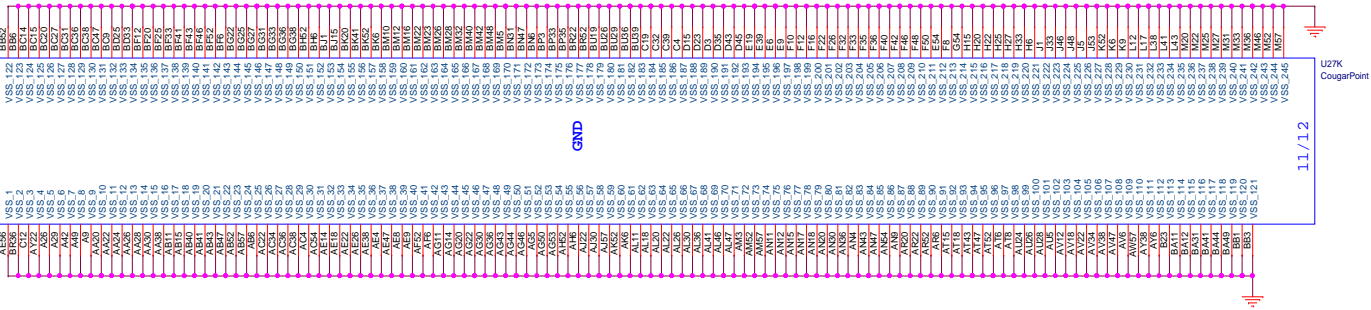




CRT for UMA Debug



CONFIDENTIAL



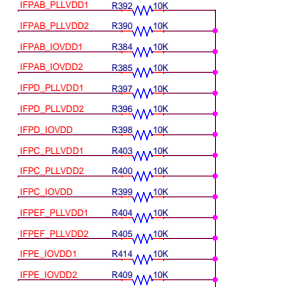
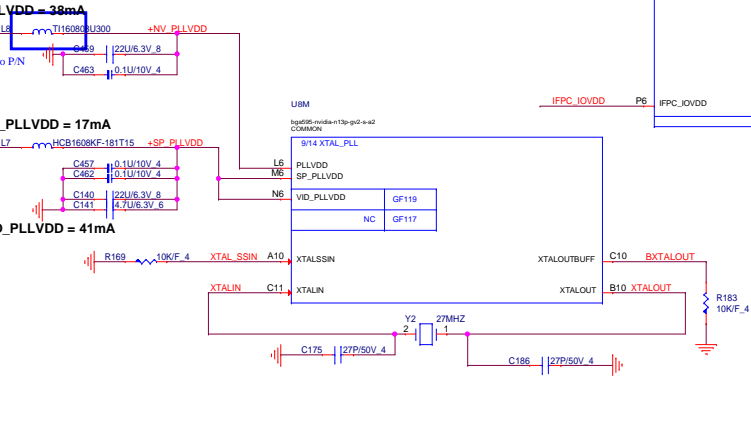
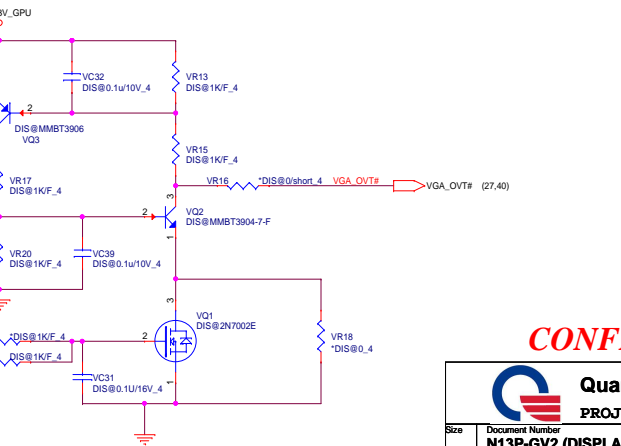
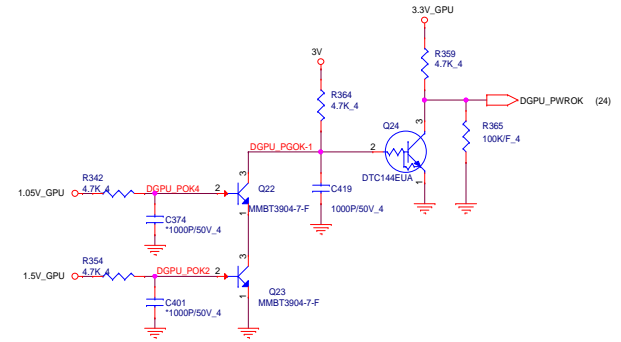
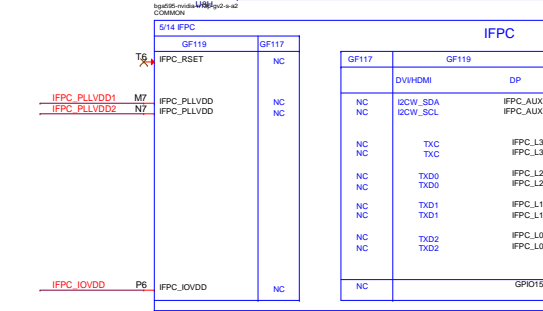
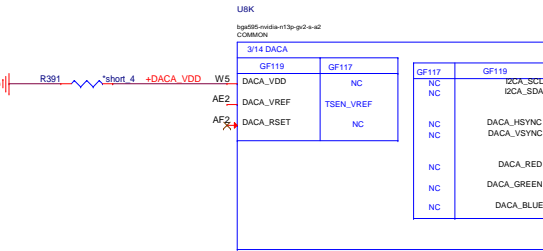
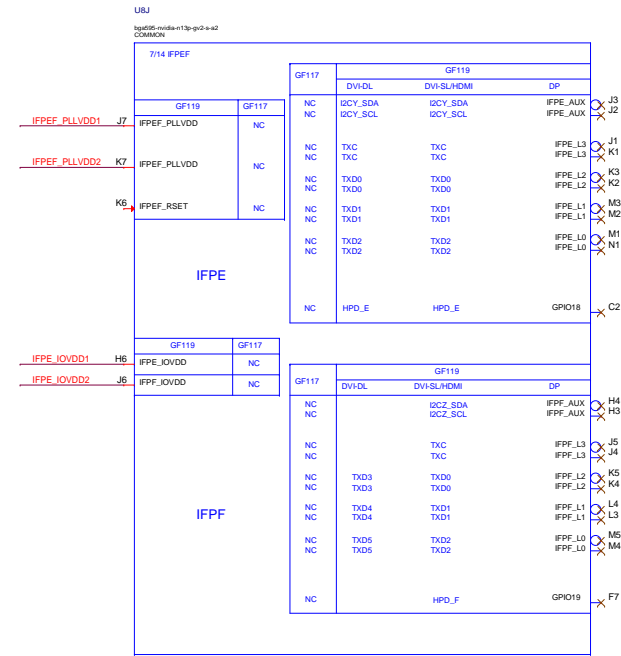
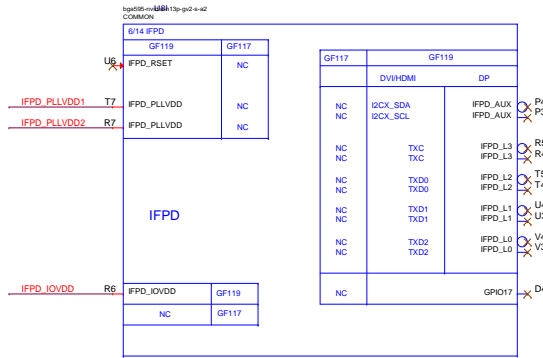
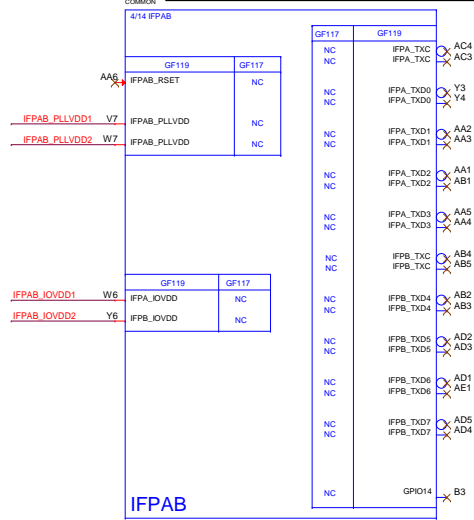
CRB0.7 pin A4,BM57 are test points

CONFIDENTIAL

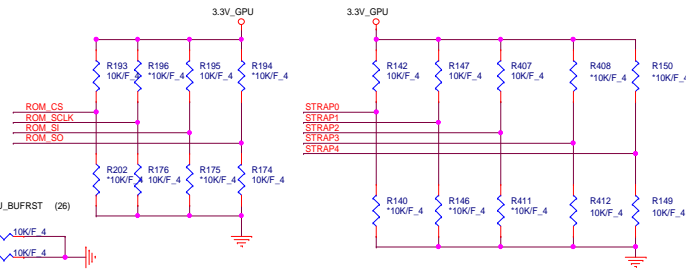
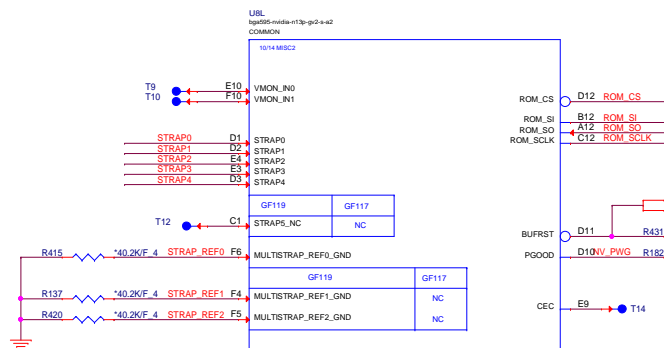
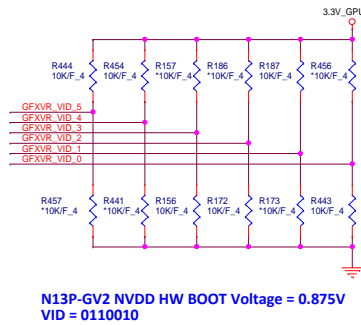


Quanta Computer Inc.
PROJECT : Xiamen/WJB

Optimus:
All unstuff , one Cap stuff 10K ohm



CONFIDENTIAL



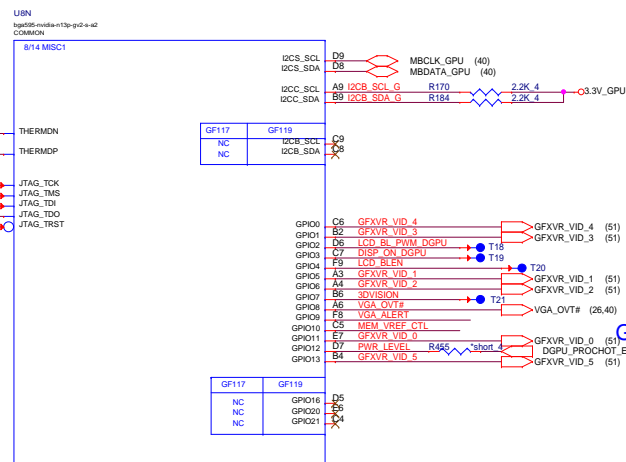
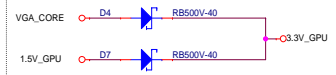
Binary Strap Mode Mapping

Strap Pin name	Strap Mapping	Resistance	Polarity
ROM_SCLK	SMB_ALT_ADDR	10Kohm	Pull-down to GND
ROM_SI	SUB_VENDOR	10Kohm	Pull-UP to 3V3 if VBIOS ROM Exists Pull-down to GND if no VBIOS ROM
ROM_SO	VGA_DEVICE	10Kohm	Pull-down to GND (no dispaly)
STRAP0	RAMCFG[0]	10Kohm	USER defined
STRAP1	RAMCFG[1]	10Kohm	USER defined
STRAP2	RAMCFG[2]	10Kohm	USER defined
STRAP3	RAMCFG[3]	10Kohm	USER defined
STRAP4	PCIE_MAX_SPEED	10Kohm	Pull-down to GND

VRAM Configuration Table

RAMCFG [3:0]	DESCRIPTION	Vendor	Vendor P/N	QC1 P/N
0000		Reserved		
0010	DDR3 64Mx16x8, 128bit, 1GB,800MHz	Hynix		
0011	DDR3 64Mx16x8, 128bit, 1GB,800MHz	Samsung		
0110	DDR3 128Mx16x4, 128bit, 1GB,900MHz	Hynix	H5TQ2G63BFR-11C	AKD5MGWTW07
1111	DDR3 128Mx16x4, 128bit, 1GB,900MHz	Samsung	K4W2G1646C-HC11	AKD5MGWTW08
XXXX				
XXXX				

for meet Power down sequence.
Nvidia request for optmuis

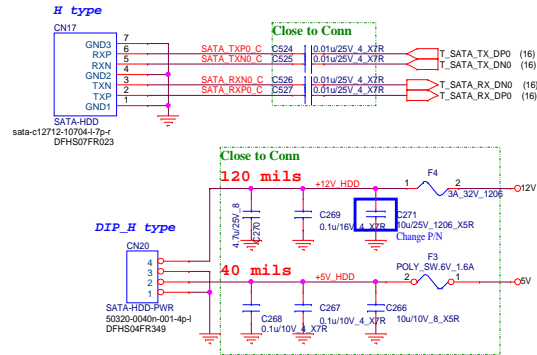


GB2-64 and GB4-128 GPIO Desdription

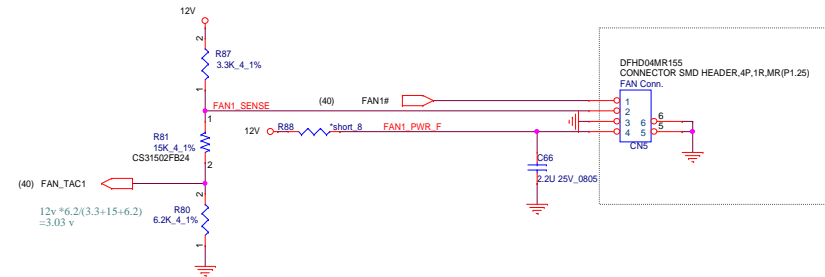
GPIO pin Name	Normal Function	I/O	Functional Description	Recommended Default Pull-up or Pull-down
GPIO0	GPU_VID4	O	GPU Core VDD VID4	Strap to boot HVVDD
GPIO1	GPU_VID3	O	GPU Core VDD VID3	Strap to boot HVVDD
GPIO2	LCD_BL_PWM	O	Panel Backlight PWM Brightness Control	100 K pull-down
GPIO3	LCD_VCC or PSI	O	Panel Power Enable or Phase Shedding	LCD_VCC: 100k pull-down; PSI: 10k pull-up or pull-down; stuff as needed to disable phase shedding by default
GPIO4	LCD_BLEH	O	Panel Backlight Enable	100 K pull-down
GPIO5	GPU_VID1	O	GPU Core VDD VID1	Strap to boot HVVDD
GPIO6	GPU_VID2	O	GPU Core VDD VID2	Strap to boot HVVDD
GPIO7	3DVision	O	3D Vision Left/Right signal	100 K pull-down
GPIO8	OVERT	I/O	Active Low Thermal Catastrophic Over Temperature	100 K pull-up
GPIO9	ALERT	I/O	Active Low Thermal Alert	100 K pull-up
GPIO10	MEM_VREF_CTL	O	Memory VREF Control	100 K pull-down
GPIO11	GPU_VID0	O	GPU Core VDD VID0	Strap to boot HVVDD
GPIO12	PWR_LEVEL	I	AC power detect or power supply overdraw input	100 K pull-up
GPIO13	GPU_VID5	O	GPU Core VDD VID5	Strap to boot HVVDD
GPIO14	HFD_AB	I	Hot Plug Detect for IFAB	See Figure 76
GPIO15	HFD_C	I	Hot Plug Detect for IFPC	See Figure 76
GPIO16	PSI or MEM_VDD_CTL	O	Phase Shedding or Memory VDD VID	PSI: 10k pull-up or pull-down; stuff as needed to disable phase shedding by default; MEM_VDD_CTL: Strap to boot FBVDD/Q
GPIO17	HFD_D	I	Hot Plug Detect for IFPD	See Figure 76
GPIO18	HFD_E	I	Hot Plug Detect for IFPE	See Figure 76
GPIO19	HFD_F	I	Hot Plug Detect for IFPF	See Figure 76
GPIO20	Reserved			
GPIO21	Reserved			

CONFIDENTIAL

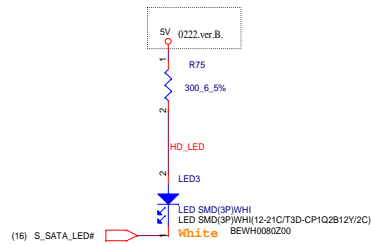
HDD SATA CONNECTOR



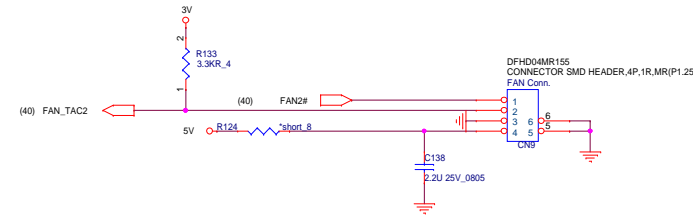
FAN1



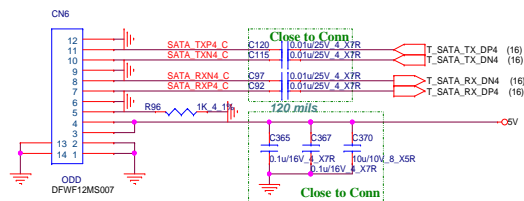
HDD LED



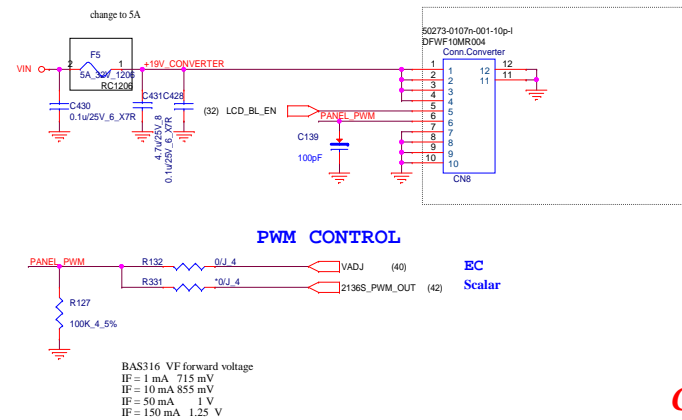
FAN2



ODD SATA Conn.

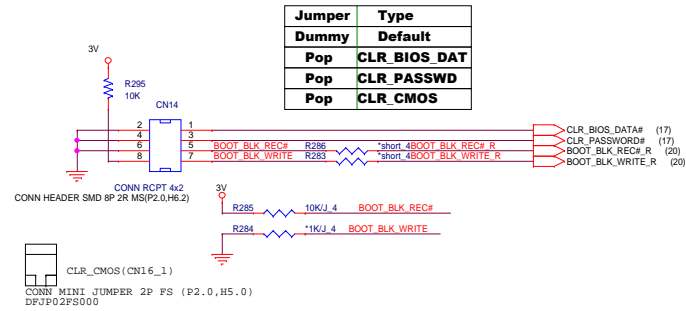


TO CONVERTER CONNECT

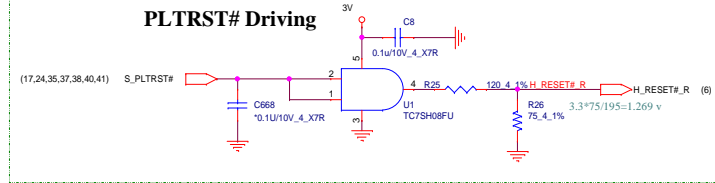


CONFIDENTIAL

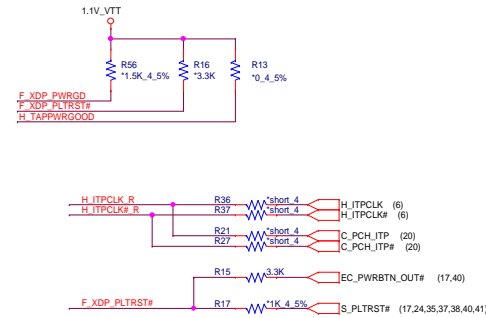
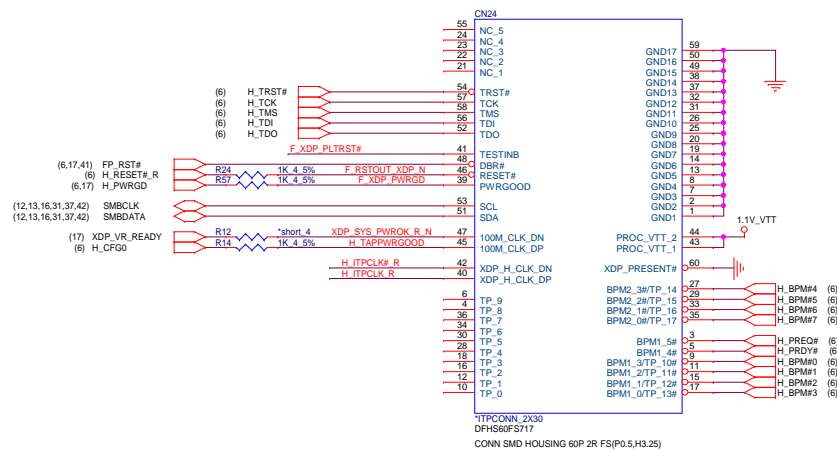
CLR_CMOS



PLTRST# Driving



XDP Connector - CPU



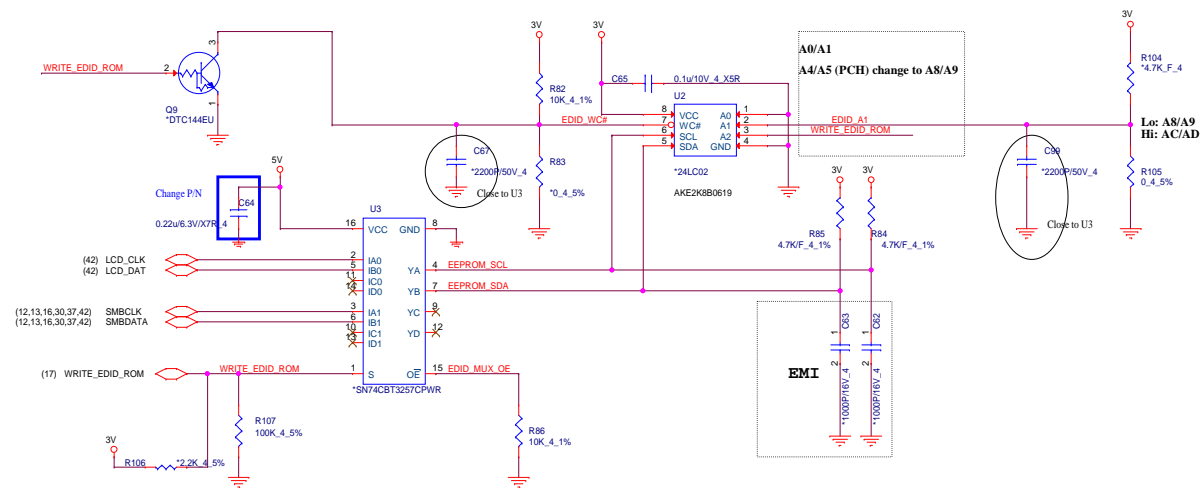
CONFIDENTIAL



Quanta Computer Inc.

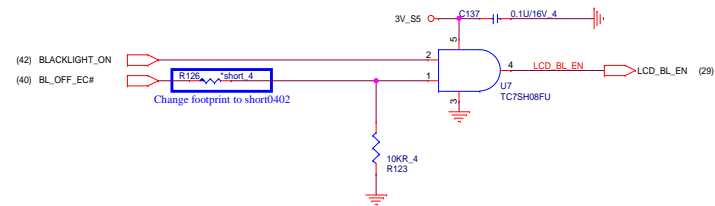
PROJECT : Xiamen/WJB

PANEL EDID DATA

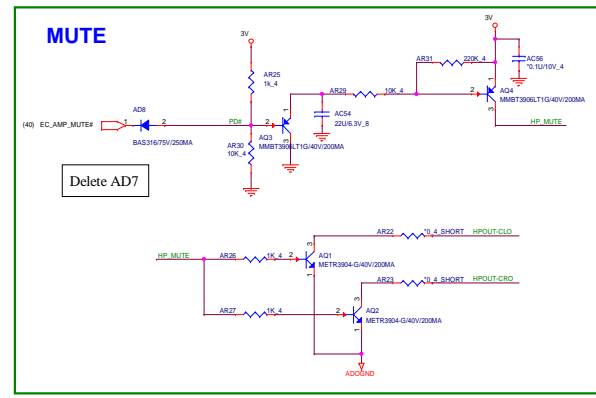


Size	Document Number	Rev
	EDID	1/
Date:	Tuesday, June 05, 2012	Sheet 31 of 53

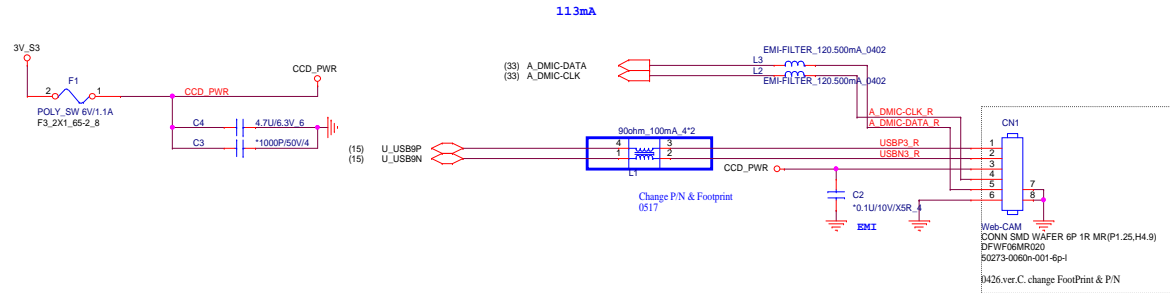
BackLight Enable

Rev
1A

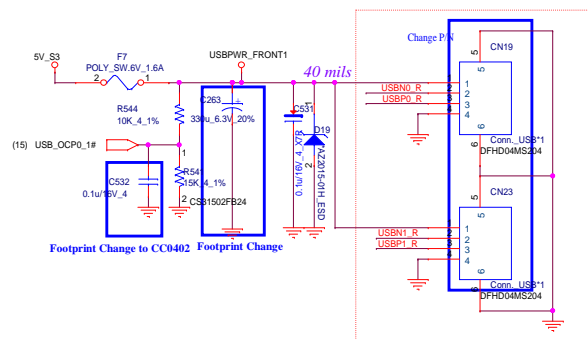
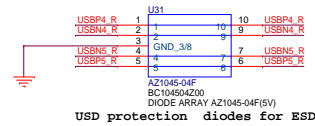
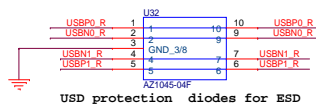
Date: Tuesday, June 05, 2012 Sheet 32 of 53



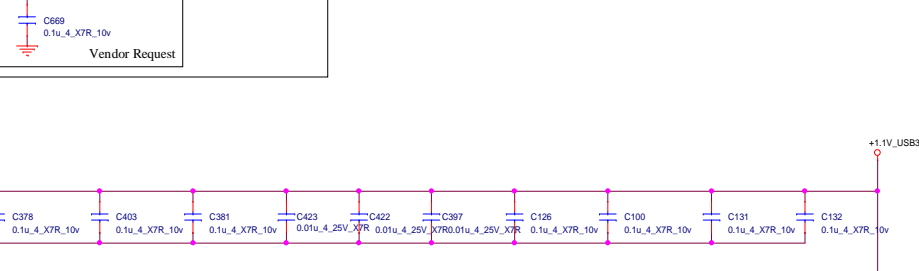
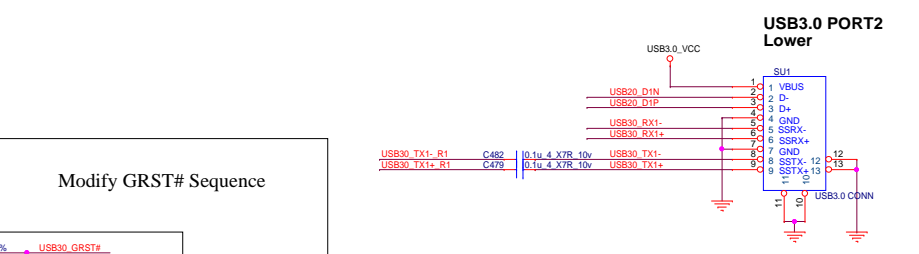
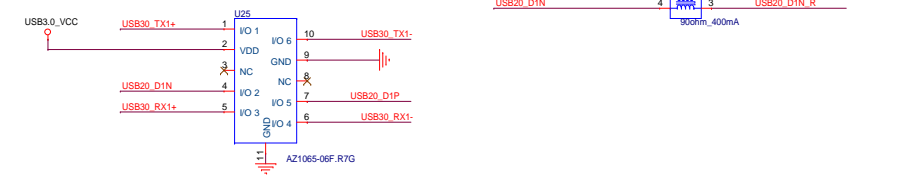
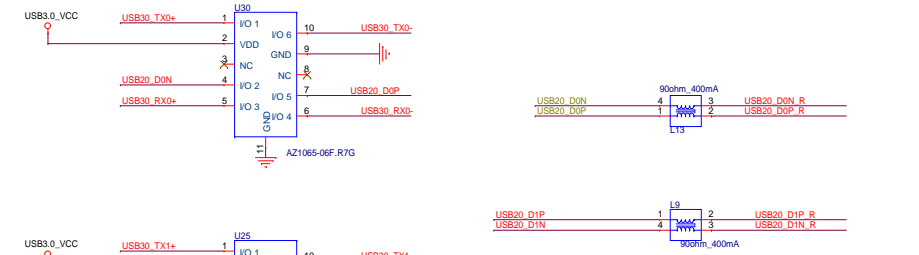
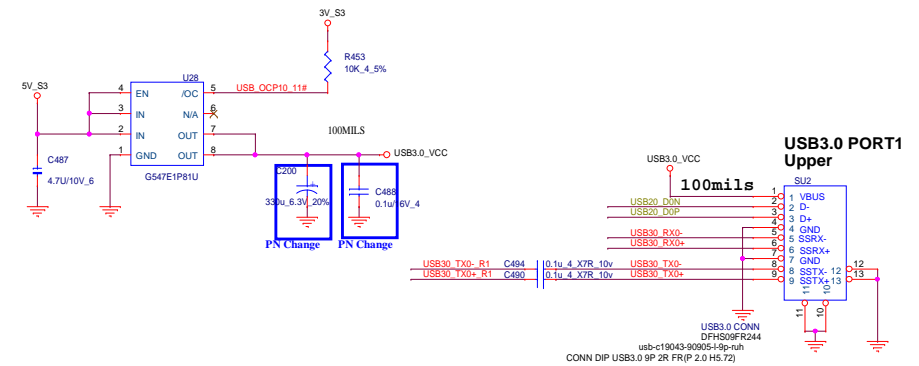
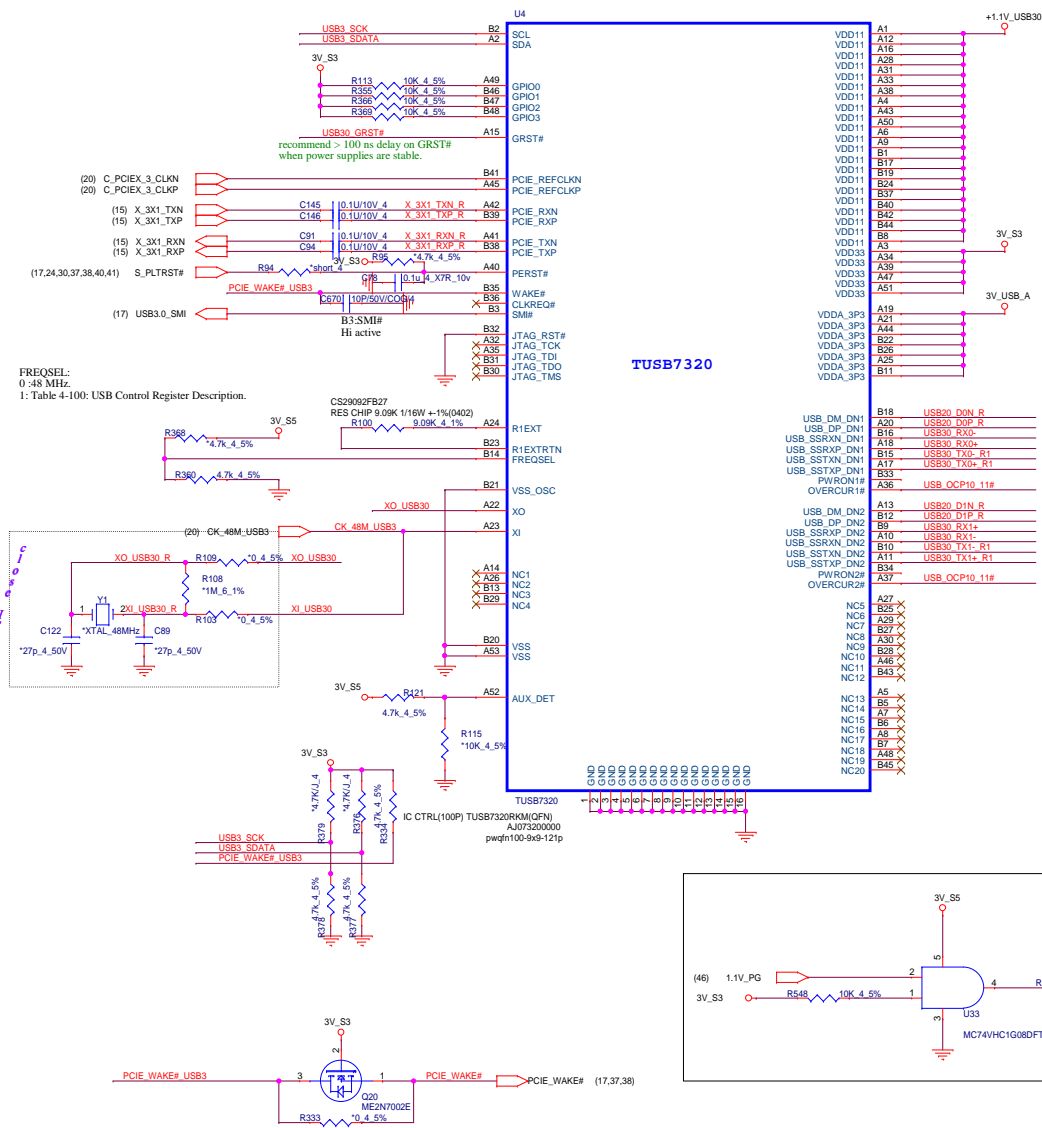
WEBCAM



4 Rear(USB2.3.4.5.) - Vertical Type

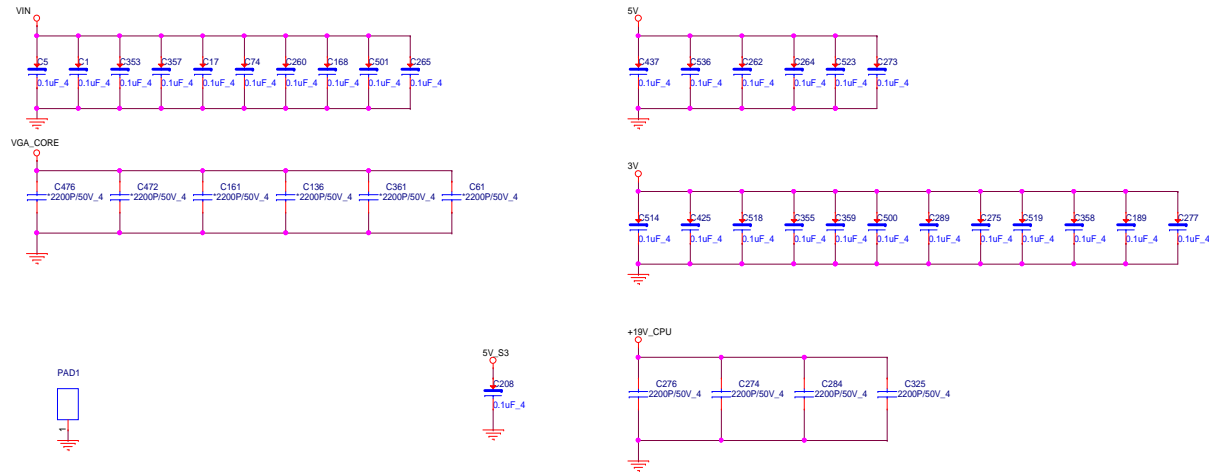


TUSB7320 in D0 state with two SS hubs connected (both SS and HS active):
VDD11 = 793 mW, VDD33 = 20 mW, VDDA_3P3 = 478 mW

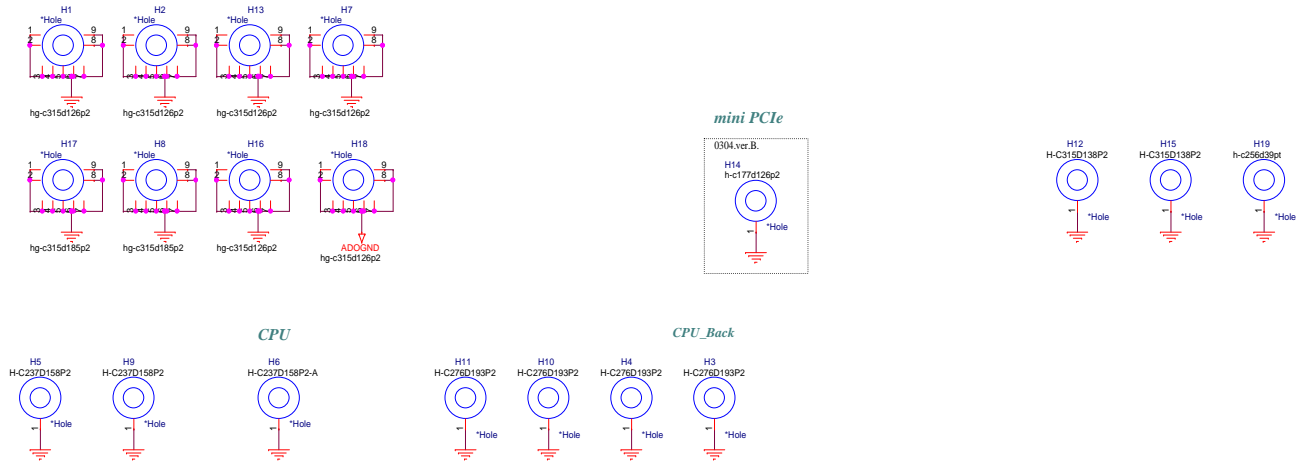


CONFIDENTIAL

EMI Cap.



Hole.



CONFIDENTIAL

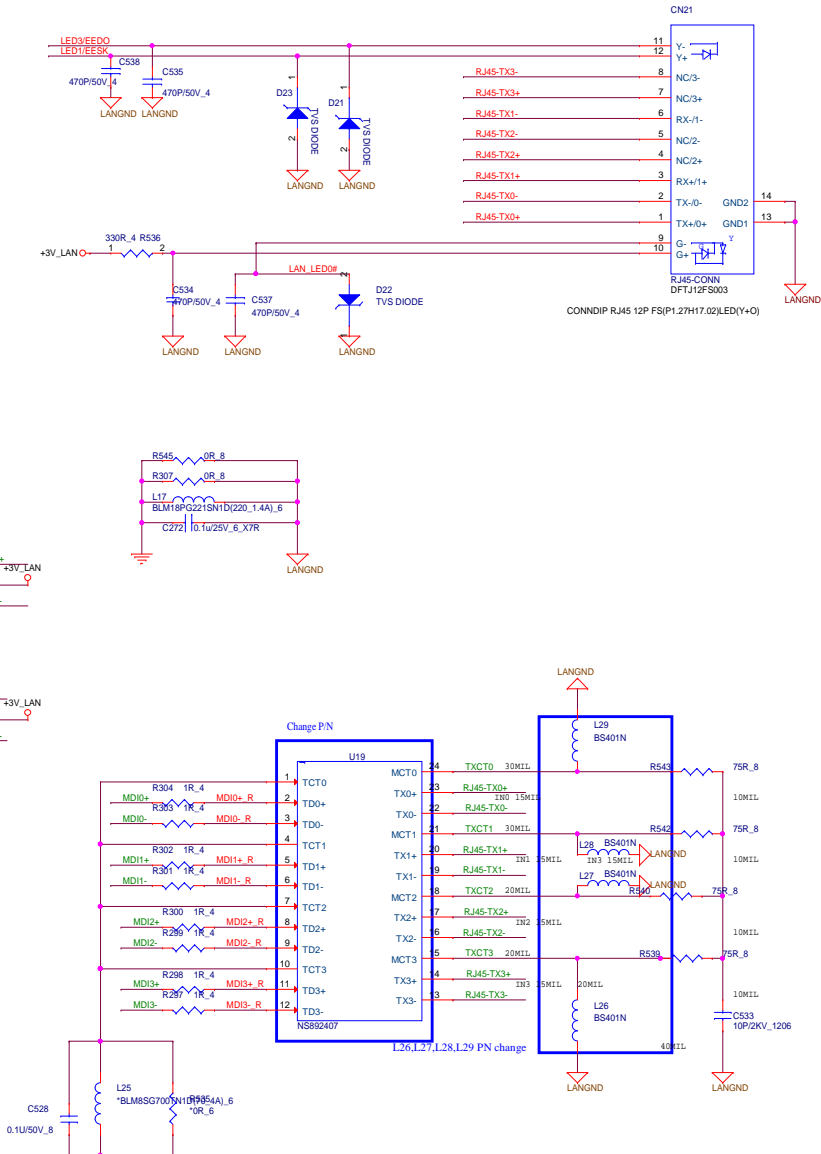
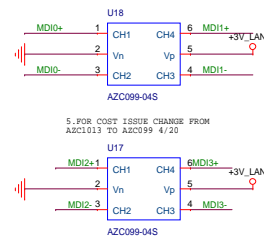
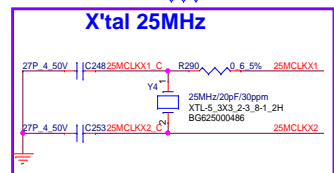
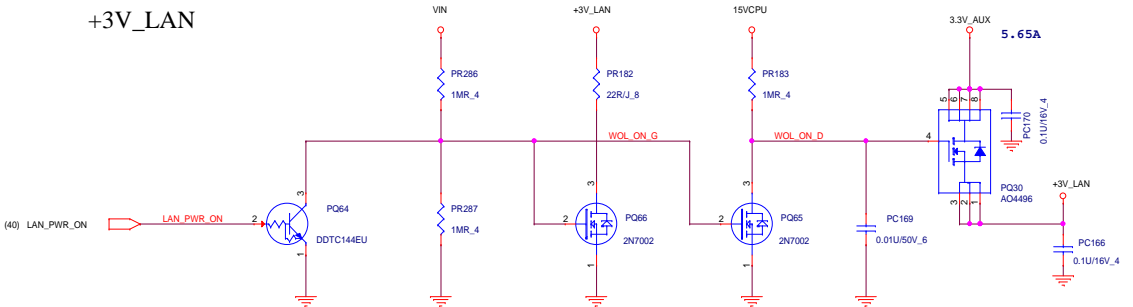
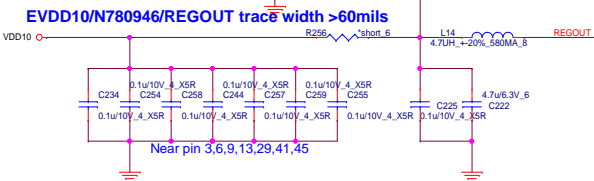
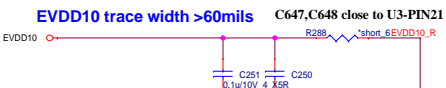
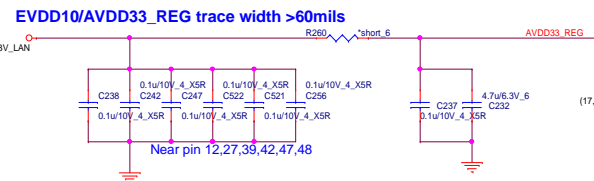
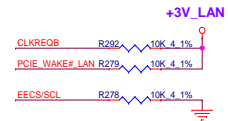
+3V_LAN

Don't support ASF function.
SMBus pin.14 N.C.
pin.15 pull Down

R274 1K 4.5% GPO

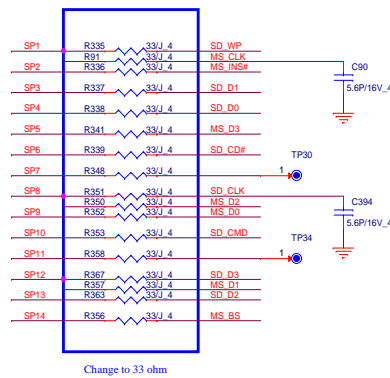
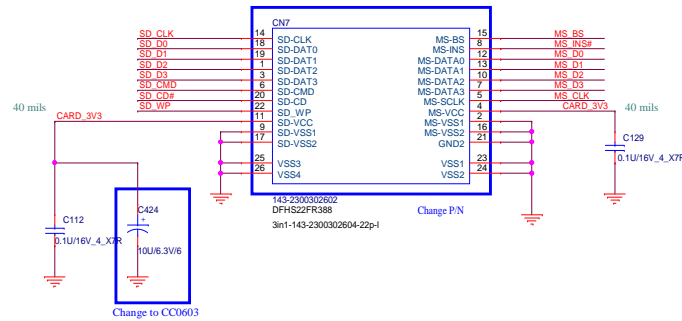
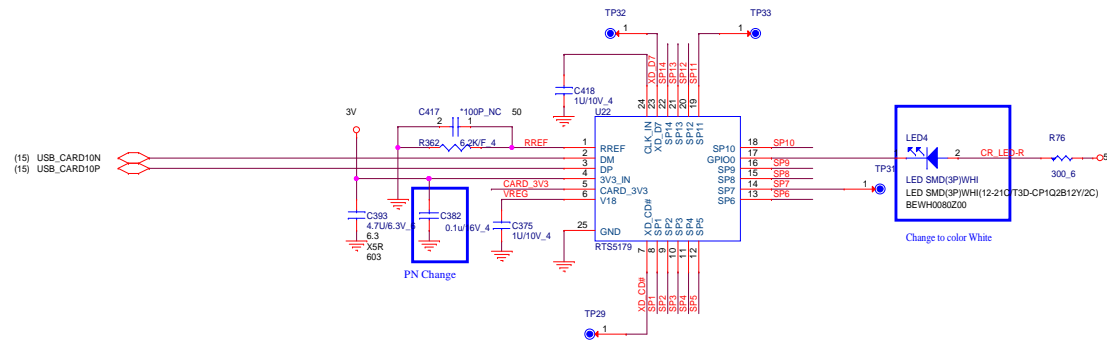
R293 10K 4.1% SMBDATA LAN

R277 10K 4.1% FEED/SDA



Quanta Computer Inc.
PROJECT : Xiamen/WJB

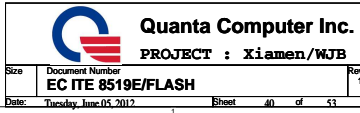
Size	Document Number	Rev
	LAN RTL8111E/RJ45	1A
Date:	Tuesday, June 05, 2012	Sheet 38 of 51



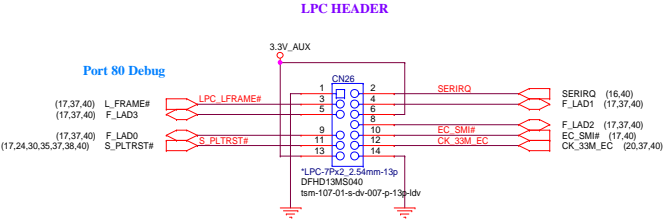
Share Pin

Share Pin	XD	MS	SD
SP1		MS_CLK	SD_WP
SP2		MS_INS#	
SP3			SD_D1
SP4			SD_D0
SP5		MS_D3	
SP6			SD_CD#
SP7			
SP8		MS_D2	SD_CLK
SP9		MS_D0	
SP10			SD_CMD
SP11			
SP12		MS_D1	SD_D3
SP13			SD_D2
SP14		MS_BS	

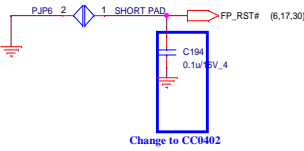
CONFIDENTIAL



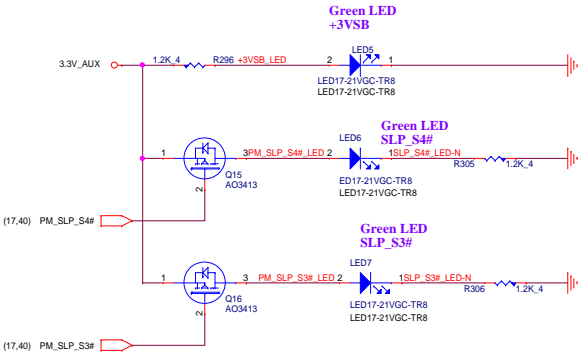
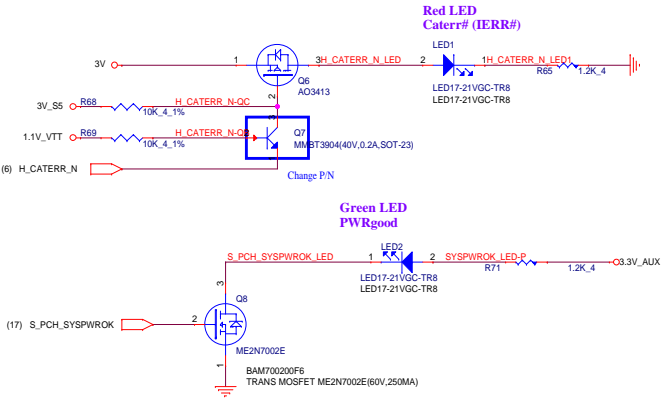
LEDs/LPC HEADER



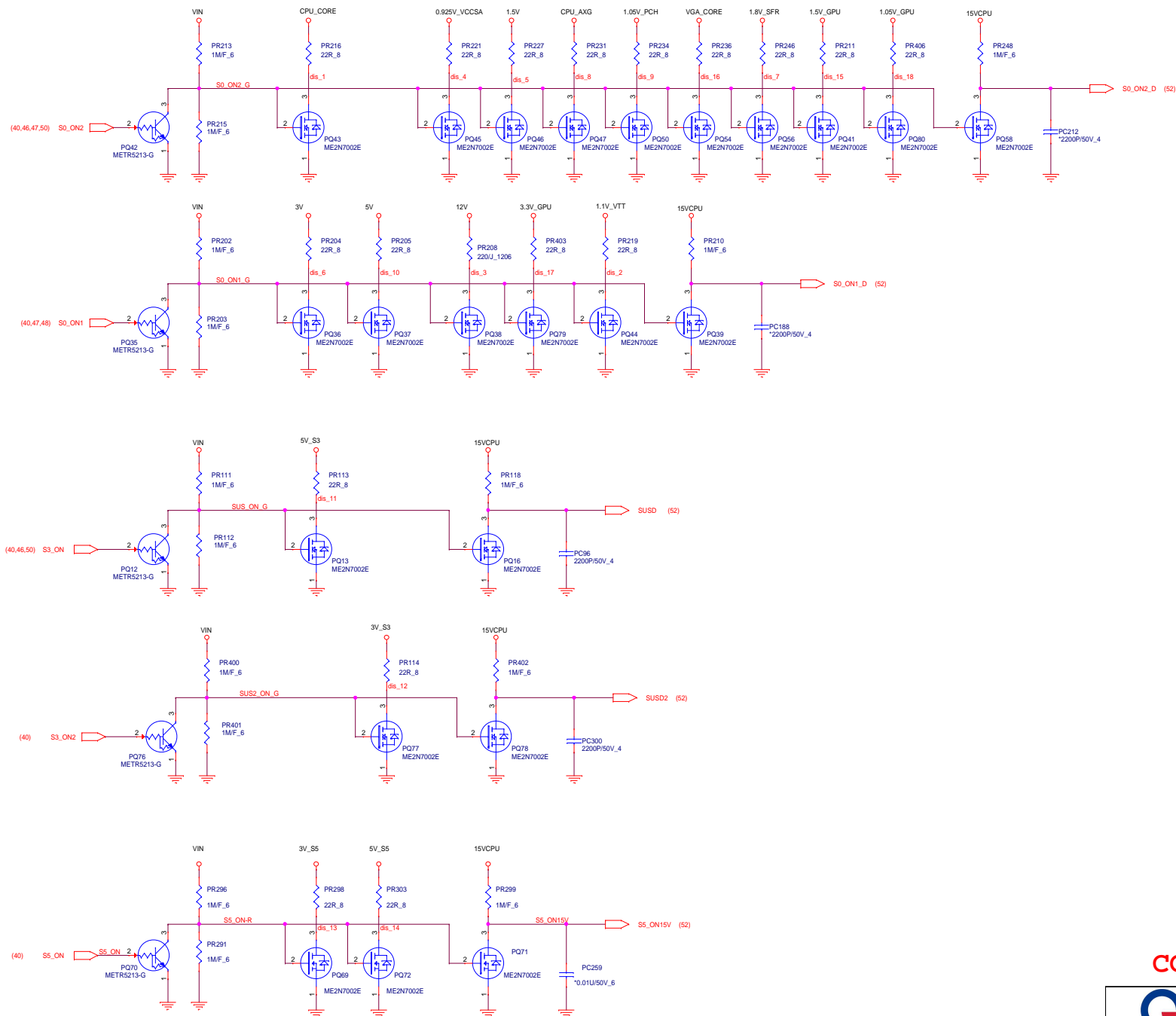
Reset Button (For Debug only)



PCA debug LED requirement:



CONFIDENTIAL



CONFIDENTIAL

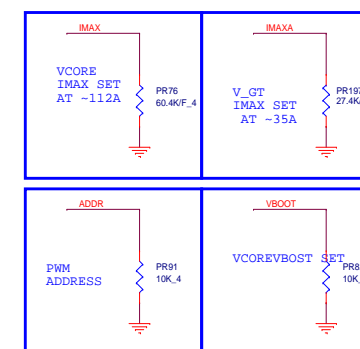
NCP6131(52pin SR) VR12 POWER CKT - 3+1 PHASE

The schematic diagram illustrates the power management circuit for the NCP6131(52pin SR) VR12, configured in a 3+1 phase mode. The circuit is divided into several functional sections:

- VCORE PORTION:** This section includes the main power regulation circuit for the Vcore. It features a 6X6 52PIN SINGLE ROW QFN package. Key components include resistors (PR17, PR21, PR23, PR15, PR22, PR30, PR19, PR27, PR18, PR33, PR35, PR39, PR40, PR36, PR38, PR34, PR32, PR31, PR37, PR38, PR39, PR40, PR41, PR42, PR43, PR44, PR45, PR46, PR47, PR48, PR49, PR50, PR51, PR52, PR53, PR54, PR55, PR56, PR57, PR58, PR59, PR60, PR61, PR62, PR63, PR64, PR65, PR66, PR67, PR68, PR69, PR70, PR71, PR72, PR73, PR74, PR75, PR76, PR77, PR78, PR79, PR80, PR81, PR82, PR83, PR84, PR85, PR86, PR87, PR88, PR89, PR90, PR91, PR92, PR93, PR94, PR95, PR96, PR97, PR98, PR99, PR100, PR101, PR102, PR103, PR104, PR105, PR106, PR107, PR108, PR109, PR110, PR111, PR112, PR113, PR114, PR115, PR116, PR117, PR118, PR119, PR120, PR121, PR122, PR123, PR124, PR125, PR126, PR127, PR128, PR129, PR130, PR131, PR132, PR133, PR134, PR135, PR136, PR137, PR138, PR139, PR140, PR141, PR142, PR143, PR144, PR145, PR146, PR147, PR148, PR149, PR150, PR151, PR152, PR153, PR154, PR155, PR156, PR157, PR158, PR159, PR160, PR161, PR162, PR163, PR164, PR165, PR166, PR167, PR168, PR169, PR170, PR171, PR172, PR173, PR174, PR175, PR176, PR177, PR178, PR179, PR180, PR181, PR182, PR183, PR184, PR185, PR186, PR187, PR188, PR189, PR190, PR191, PR192, PR193, PR194, PR195, PR196, PR197, PR198, PR199, PR200, PR201, PR202, PR203, PR204, PR205, PR206, PR207, PR208, PR209, PR210, PR211, PR212, PR213, PR214, PR215, PR216, PR217, PR218, PR219, PR220, PR221, PR222, PR223, PR224, PR225, PR226, PR227, PR228, PR229, PR230, PR231, PR232, PR233, PR234, PR235, PR236, PR237, PR238, PR239, PR240, PR241, PR242, PR243, PR244, PR245, PR246, PR247, PR248, PR249, PR250, PR251, PR252, PR253, PR254, PR255, PR256, PR257, PR258, PR259, PR260, PR261, PR262, PR263, PR264, PR265, PR266, PR267, PR268, PR269, PR270, PR271, PR272, PR273, PR274, PR275, PR276, PR277, PR278, PR279, PR280, PR281, PR282, PR283, PR284, PR285, PR286, PR287, PR288, PR289, PR290, PR291, PR292, PR293, PR294, PR295, PR296, PR297, PR298, PR299, PR300, PR301, PR302, PR303, PR304, PR305, PR306, PR307, PR308, PR309, PR310, PR311, PR312, PR313, PR314, PR315, PR316, PR317, PR318, PR319, PR320, PR321, PR322, PR323, PR324, PR325, PR326, PR327, PR328, PR329, PR330, PR331, PR332, PR333, PR334, PR335, PR336, PR337, PR338, PR339, PR340, PR341, PR342, PR343, PR344, PR345, PR346, PR347, PR348, PR349, PR350, PR351, PR352, PR353, PR354, PR355, PR356, PR357, PR358, PR359, PR360, PR361, PR362, PR363, PR364, PR365, PR366, PR367, PR368, PR369, PR370, PR371, PR372, PR373, PR374, PR375, PR376, PR377, PR378, PR379, PR380, PR381, PR382, PR383, PR384, PR385, PR386, PR387, PR388, PR389, PR390, PR391, PR392, PR393, PR394, PR395, PR396, PR397, PR398, PR399, PR400, PR401, PR402, PR403, PR404, PR405, PR406, PR407, PR408, PR409, PR410, PR411, PR412, PR413, PR414, PR415, PR416, PR417, PR418, PR419, PR420, PR421, PR422, PR423, PR424, PR425, PR426, PR427, PR428, PR429, PR430, PR431, PR432, PR433, PR434, PR435, PR436, PR437, PR438, PR439, PR440, PR441, PR442, PR443, PR444, PR445, PR446, PR447, PR448, PR449, PR450, PR451, PR452, PR453, PR454, PR455, PR456, PR457, PR458, PR459, PR460, PR461, PR462, PR463, PR464, PR465, PR466, PR467, PR468, PR469, PR470, PR471, PR472, PR473, PR474, PR475, PR476, PR477, PR478, PR479, PR480, PR481, PR482, PR483, PR484, PR485, PR486, PR487, PR488, PR489, PR490, PR491, PR492, PR493, PR494, PR495, PR496, PR497, PR498, PR499, PR500, PR501, PR502, PR503, PR504, PR505, PR506, PR507, PR508, PR509, PR510, PR511, PR512, PR513, PR514, PR515, PR516, PR517, PR518, PR519, PR520, PR521, PR522, PR523, PR524, PR525, PR526, PR527, PR528, PR529, PR530, PR531, PR532, PR533, PR534, PR535, PR536, PR537, PR538, PR539, PR540, PR541, PR542, PR543, PR544, PR545, PR546, PR547, PR548, PR549, PR550, PR551, PR552, PR553, PR554, PR555, PR556, PR557, PR558, PR559, PR560, PR561, PR562, PR563, PR564, PR565, PR566, PR567, PR568, PR569, PR570, PR571, PR572, PR573, PR574, PR575, PR576, PR577, PR578, PR579, PR580, PR581, PR582, PR583, PR584, PR585, PR586, PR587, PR588, PR589, PR590, PR591, PR592, PR593, PR594, PR595, PR596, PR597, PR598, PR599, PR600, PR601, PR602, PR603, PR604, PR605, PR606, PR607, PR608, PR609, PR610, PR611, PR612, PR613, PR614, PR615, PR616, PR617, PR618, PR619, PR620, PR621, PR622, PR623, PR624, PR625, PR626, PR627, PR628, PR629, PR630, PR631, PR632, PR633, PR634, PR635, PR636, PR637, PR638, PR639, PR640, PR641, PR642, PR643, PR644, PR645, PR646, PR647, PR648, PR649, PR650, PR651, PR652, PR653, PR654, PR655, PR656, PR657, PR658, PR659, PR660, PR661, PR662, PR663, PR664, PR665, PR666, PR667, PR668, PR669, PR670, PR671, PR672, PR673, PR674, PR675, PR676, PR677, PR678, PR679, PR680, PR681, PR682, PR683, PR684, PR685, PR686, PR687, PR688, PR689, PR690, PR691, PR692, PR693, PR694, PR695, PR696, PR697, PR698, PR699, PR700, PR701, PR702, PR703, PR704, PR705, PR706, PR707, PR708, PR709, PR710, PR711, PR712, PR713, PR714, PR715, PR716, PR717, PR718, PR719, PR720, PR721, PR722, PR723, PR724, PR725, PR726, PR727, PR728, PR729, PR730, PR731, PR732, PR733, PR734, PR735, PR736, PR737, PR738, PR739, PR740, PR741, PR742, PR743, PR744, PR745, PR746, PR747, PR748, PR749, PR750, PR751, PR752, PR753, PR754, PR755, PR756, PR757, PR758, PR759, PR760, PR761, PR762, PR763, PR764, PR765, PR766, PR767, PR768, PR769, PR770, PR771, PR772, PR773, PR774, PR775, PR776, PR777, PR778, PR779, PR780, PR781, PR782, PR783, PR784, PR785, PR786, PR787, PR788, PR789, PR790, PR791, PR792, PR793, PR794, PR795, PR796, PR797, PR798, PR799, PR800, PR801, PR802, PR803, PR804, PR805, PR806, PR807, PR808, PR809, PR810, PR811, PR812, PR813, PR81

PWM ADDRESS		
RESISTOR VALUE	SVID ADDRESS FOR VCOE RAIL	SVID ADDRESS FOR V_GT RAIL
10K	0000	0001
25K	0010	0011
45K	0100	0101
70K	0110	0111
95K	1000	1001
125K	1010	1011
165K	1100	1101

BOOT VOLTAGE	
RESISTOR VALUE	BOOT VOLTAGE
10K	0V
25K	0.85V
45K	0.9V
70K	0.95V
95K	1V
125K	1.1V
165K	1.5V



CONFIDENTIAL

**Quanta Computer Inc.**

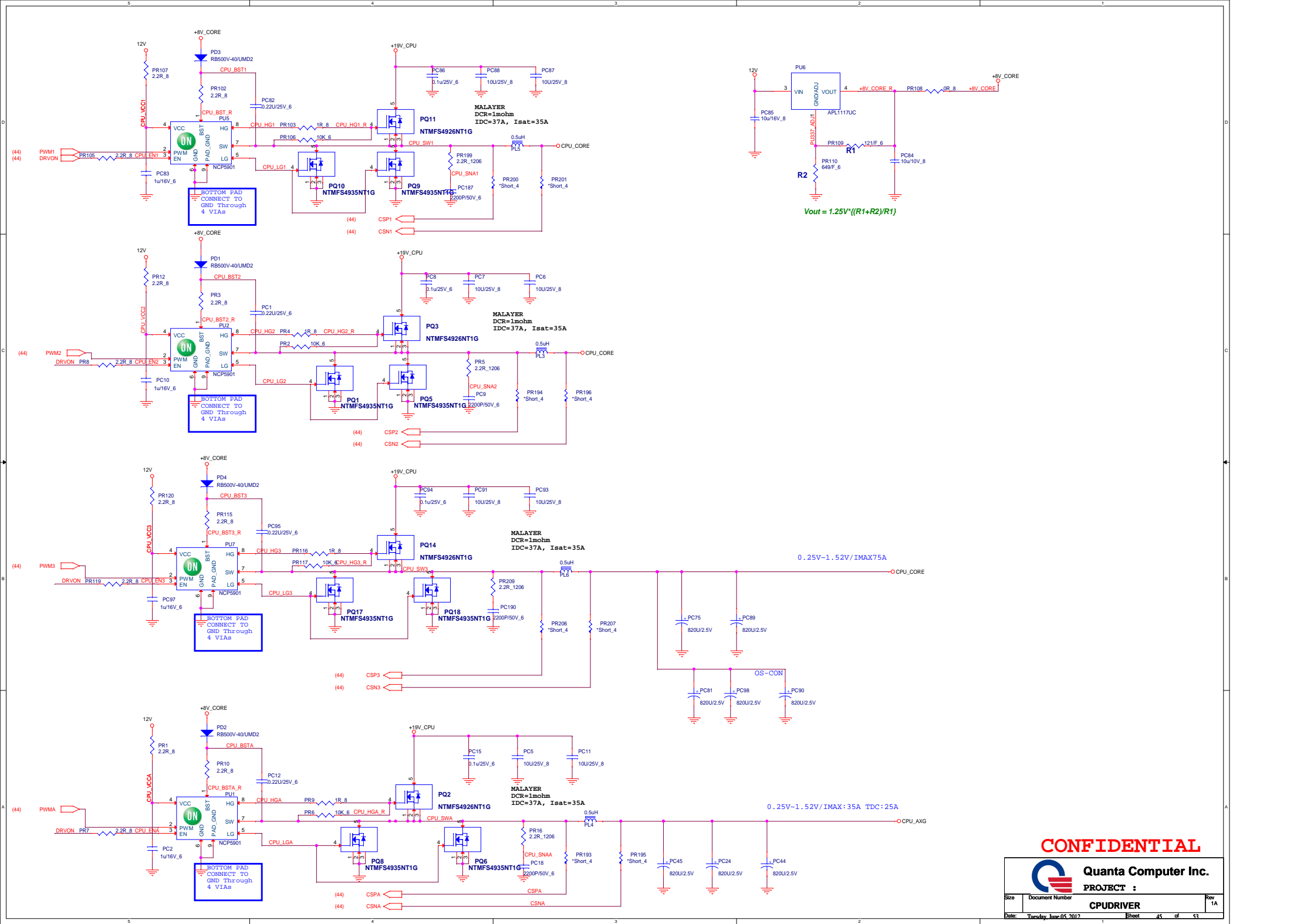
PROJECT :

CPU(NCP6131S)

Rev	1A
-----	----

Date: Tuesday, June 05, 2012

Sheet 44 of 44



CONFIDENTIAL

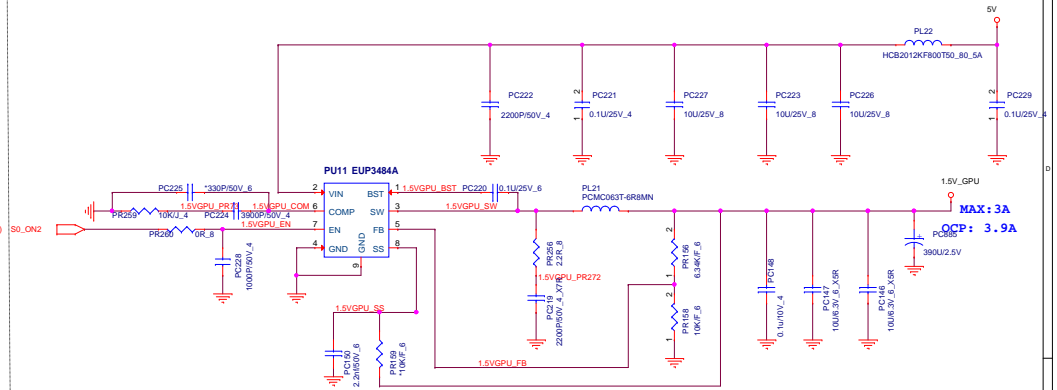


Quanta Computer Inc.

PROJECT :

CPUDRIVER

Size: Document Number: Rev 1A
 Date: Tuesday, June 05, 2012 Sheet 45 of 53

[illegible]

For PCH

1.05V
IMAX:8A TDC:5.6A
1.8W

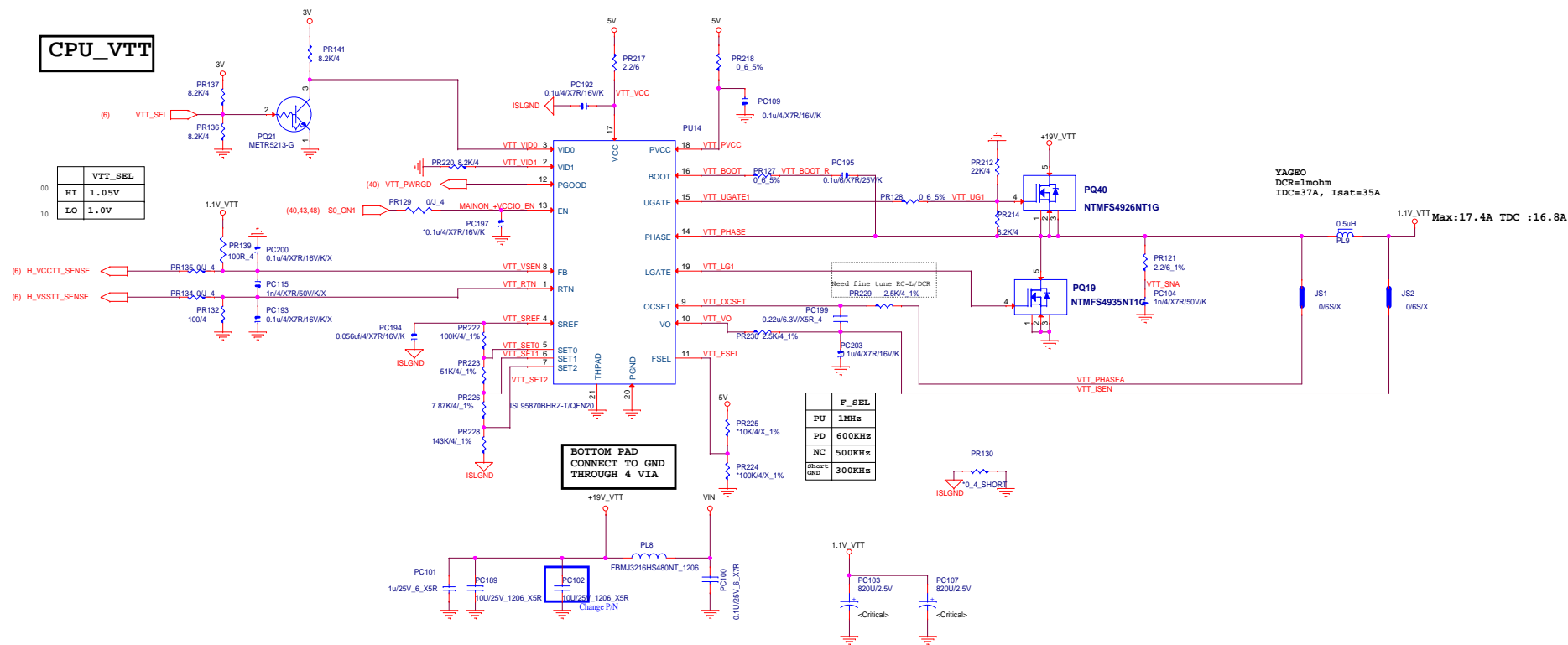
[illegible][illegible]

CONFIDENTIAL



Quanta Computer Inc.
PROJECT : Xiamen/WJB

CPU_VTT

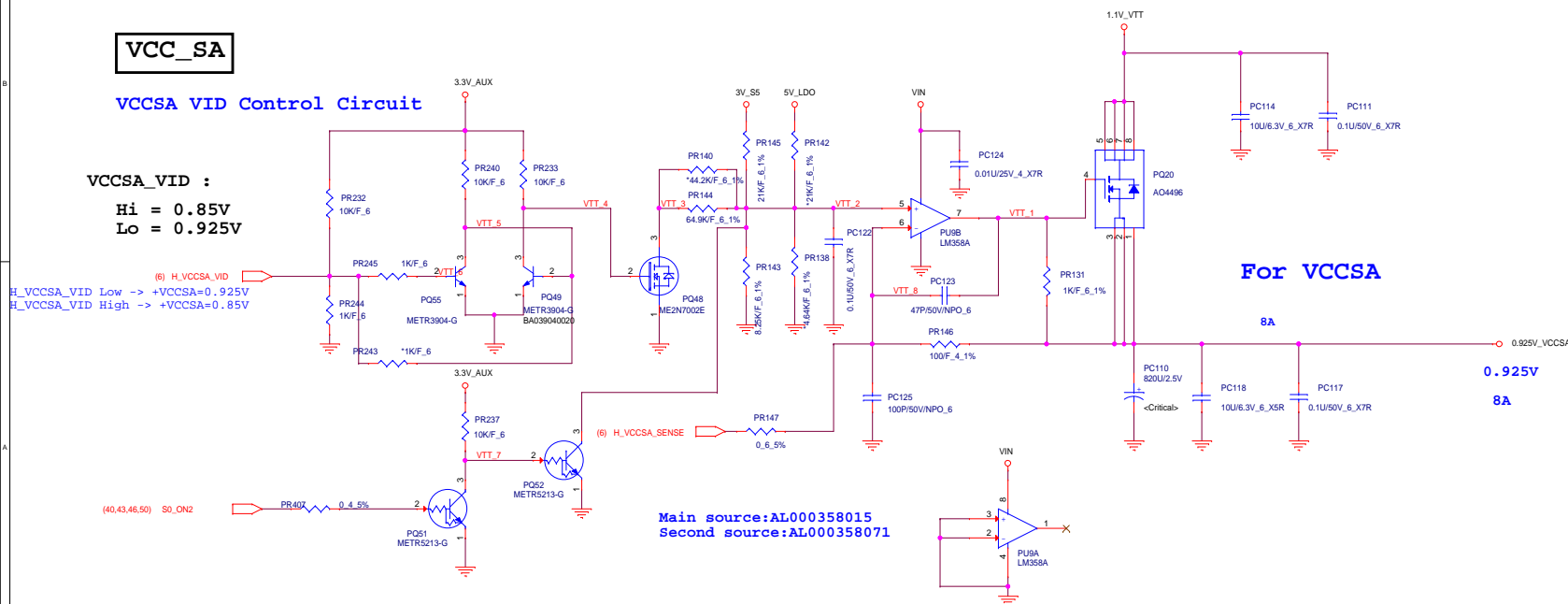


VCC_SA

VCCSA VID Control Circuit

VCCSA_VID :

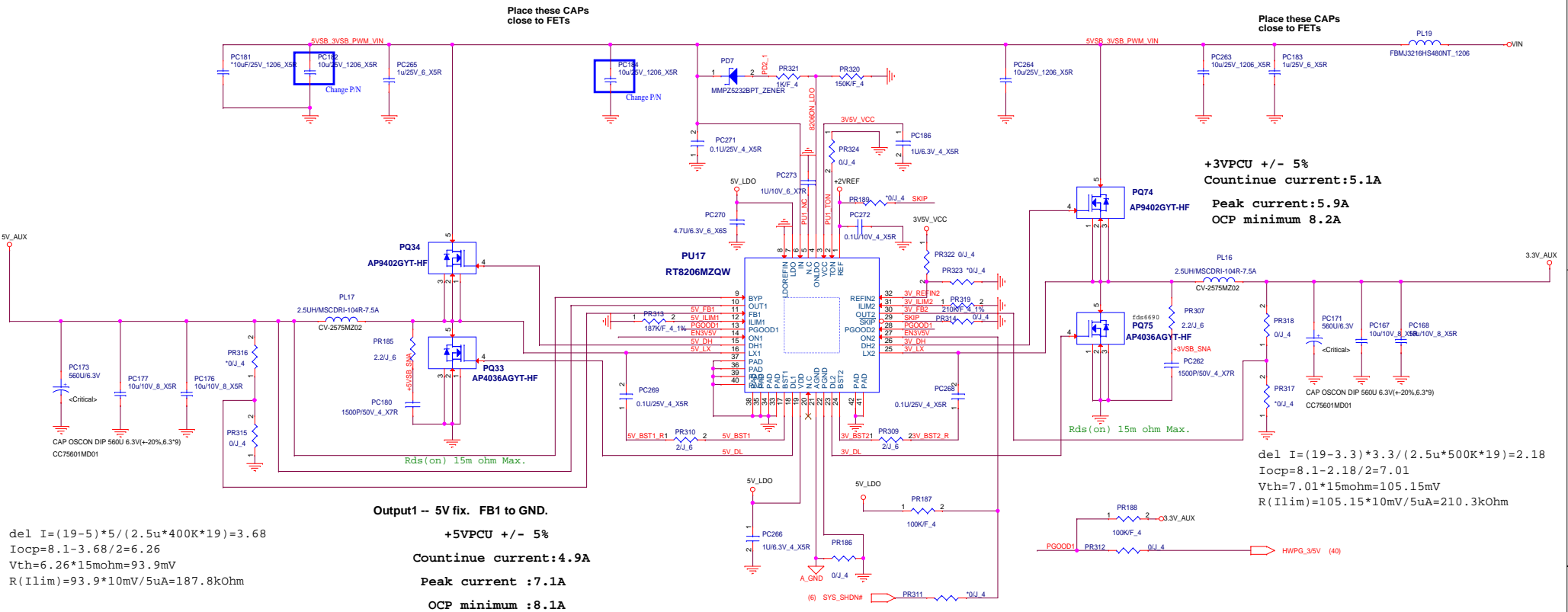
Hi = 0.85V
Lo = 0.925V



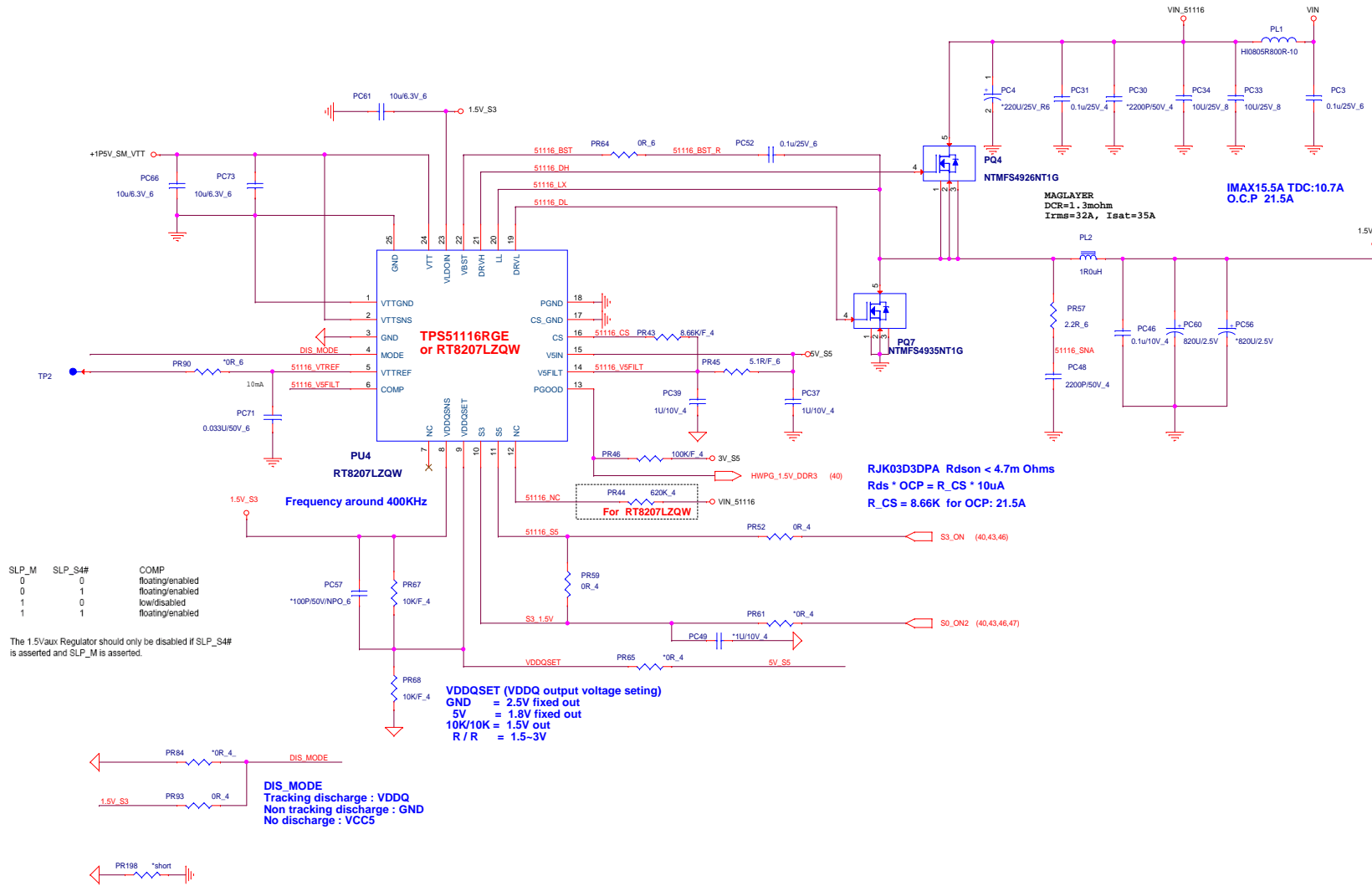
PDG 0.8

VSA_SEL	HI	LO
HI	0.85V	
LO		0.925V

CONFIDENTIAL

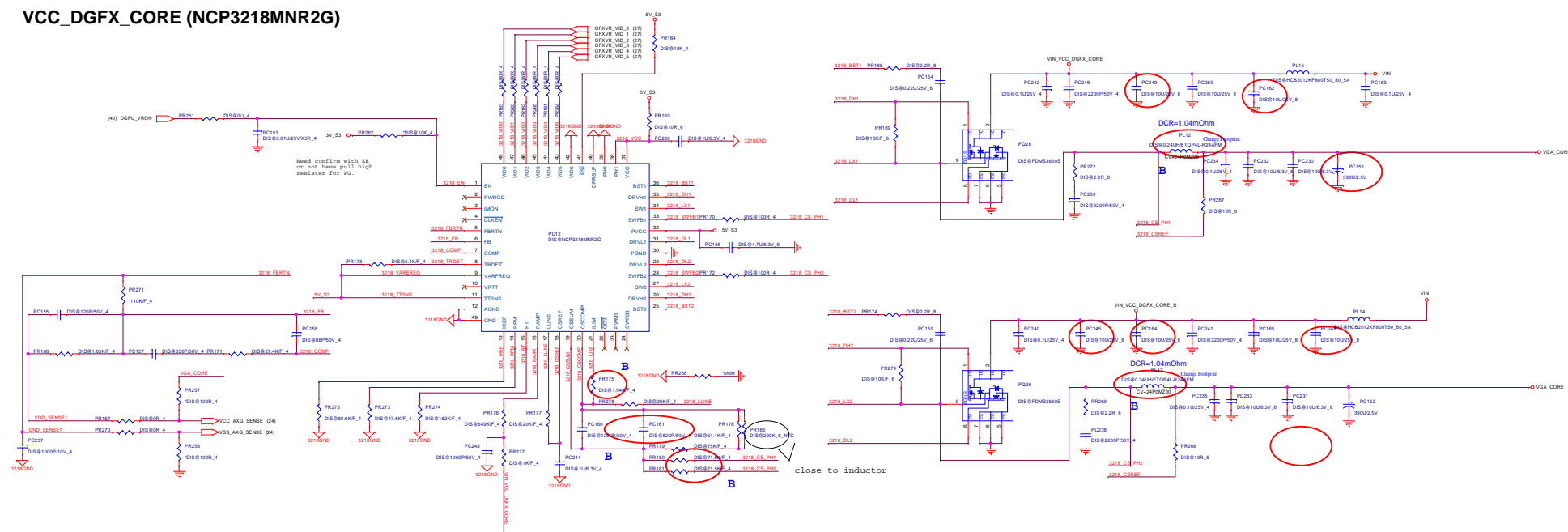


DDR POWER --1.5VSUS&VTT

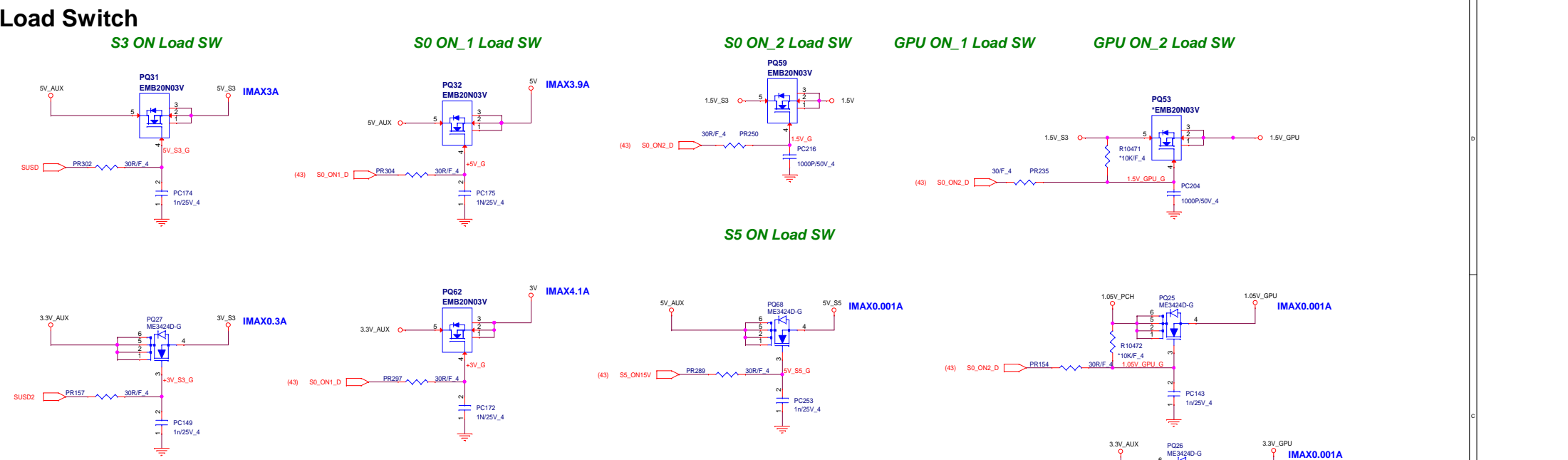


CONFIDENTIAL

VCC_DGFX_CORE (NCP3218MNR2G)



CONFIDENTIAL



Mosfet parameter

Mosfet	Package	ID(Ta=25C)	Rds_on_max	Vgs_max
AO4468	SO-8	10A/11.6A	22m	+/- 20V
AO4496	SO-8	7.5A/10.0A	26m	+/- 20V
Si4128DY	SO-8	7.0A/10.9A	30m	+/- 20V
Si4134DY	SO-8	7.0A/14A	17.5m	+/- 20V
AO3404	SOT-23	5.0A/5.8A	43m	+/- 20V
ME3424D	TSOP-6	5.0A/6.7A	42m	+/- 20V